



# Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073

## INTERNAL QUALITY ASSURANCE CELL (IQAC)

Date of Submission	03.08.2017
Type of Documents	SEMINAR
Description	SEMINAR ON ELECTRONIC SYSTEM DESIGN
Enclosures	a) REQUISITION LETTER
	b) PERMISSION LETTER
	c) SCHEDULE
	d) CIRCULAR
	e) INVITATION
	f) LIST OF PARTICIPANTS
	g) CERTIFICATE
	h) BROCHURE
	i) FEEDBACK FORM FROM PARTICIPANTS
No. of Pages	13
Submitted By	Name: Dr. M. Sangeetha
	Designation: Professor & HOD/ECE
	Department: ECE
	Signature:

### DOCUMENTS SUBMISSION FORM

#### For Office Use Only

Verified By:	K. Bakthivel	Sign:	Date: 03/8/2017
Uploaded By:	K. Senthil Kumar	Sign:	Date: 3/8/2017
File Name:	SEMI - SEE - ECE - 2017 - 2018 - 05		

Director IQAC - BIHER

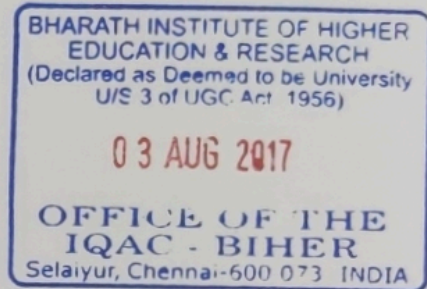




Date:20.07.2017

**REQUISITION LETTER**

From  
The HOD,  
Department of ECE,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai-73.



To  
The Dean Engineering,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai-73.

Respected Sir,

SUB: Requisition for conducting Seminar on Electronic System Design-Regd

Department of Electronics and Communication Engineering of School of Electrical Engineering has planned to conduct Seminar on Electronic System Design on 03-08-2017 between 9:30 PM to 4:30 PM. In this regard we kindly request you to grant permission for the same.

Thanking you

**HOD/ECE**

**Dean Engineering**



**Bharath**  
**INSTITUTE OF HIGHER EDUCATION AND RESEARCH**  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**  
No.173, Agharam Road, Selalpur, Chennai , T.N - 600 073.

20.07.2017

From  
The HOD,  
Department of ECE,  
School of Electrical Engineering,  
BIHER.

To  
Mr. V.Raghav,  
Senior Manager,  
VI Microsystems Pvt.Ltd,  
Chennai

Respected sir,

Sub:-The Department of Electronics and Communication Engineering, School of Electrical Science, BIHER, proposed to conduct a seminar in "ElectronicSystemDesign"- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in "ElectronicSystemDesign" on 03.08.2017. Kindly accept our request and do the needful .

Convener

**Dr.M.Sangeetha,**



**DEPARTMENT OF ECE**

**Program Schedule**

**03/08/2017(Thursday)**

09.30 - 10.00 AM	Inaugural Address
09.30 AM	Welcome Address
09.35 AM	Dr.M.Sangeetha
09.40AM	About the Seminar
09.45 AM	Ms.K.Subbulakshmi
10.00 AM Onwards	Introduction of the Chief Guest
	Dr.S.Arulsevi
	Inaugural address by Chief Guest
	Mr. V.Raghav, Senior Manager,
	VI Microsystems Pvt. Ltd,
	Chennai- 600 096
	Invited Talk
	Mr. V.Raghav, Senior Manager,
	VI Microsystems Pvt. Ltd,
	Chennai- 600 096
12.30-1.30 PM	Lunch break
1.45-03.00pm	Presentation

**Valedictory program**

03.30-04.00PM	Valedictory Function
04.00-04.20PM	Vote of Thanks (Coordinator)



# Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Date:25-07-2017

## CIRCULAR

### SCHOOL OF ELECTRICAL ENGINEERING

Seminar on 'Electronic System Design' is planned by Department of Electronics and Communication Engineering of School of Electrical Engineering which commences on 03-08-2017 in association with VI Microsystems Pvt. Ltd, Chennai, between 9:30 AM to 4:30 PM. In this regard the students are requested to give their willingness to Class in-charge/ Seminar Coordinators. It is instructed to actively participate and get benefited by attending the seminar.

Resource Person : Mr. V Raghav  
VI microsystems Pvt. Ltd.  
Chennai

Convener: Dr.M.Sangeetha,  
HOD/ECE

Coordinator: Ms. S. Arulselvi  
Ms.K.Subbulakshmi

Contact No: 8870334286 / 9176251624  
Email id: arulselvi2003@gmail.com/ psudha.50@gmail.com

(Dr.M.Sangeetha)  
HOD/ECE

To,  
Copy to ECE Department,



**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**  
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

## SCHOOL OF ELECTRICAL ENGINEERING

### Seminar on Electronic System Design

### Invitation



**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)  
ACCREDITED WITH 'A' GRADE BY NAAC

School of Electrical Engineering

Seminar on Electronic System Design



03-08-2017

*Resource Person* : Mr. Raghav VI microsystems Pvt. Ltd.  
Chennai



09:30 am to 04:20 pm

*Keynote Address* : Dr. J. Hameed Hussain, Dean Engineering,  
BIHER, Chennai

*Special Address* : Dr. M. Sangeetha, Professor & Head,  
ECE Dept, BIHER, Chennai

Venue: Smart room, ECE, BIHER

*Co-ordinators* : Dr. S. Arulselvi  
Ms. K. Subbulakshmi

Organized by  
Department of Electronics and Communication Engineering  
School of Electrical Engineering  
Bharath Institute of Higher Education and Research  
Selaiyur, Chennai

**Certificates will be provided to all participants**



**Bharath**  
**INSTITUTE OF HIGHER EDUCATION AND RESEARCH**  
 (Declared as Deemed - to - be - University under section 3 of UGC Act 1956)





**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**  
 No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

**SCHOOL OF ELECTRICAL ENGINEERING**

**Seminar on Electronic System Design**

**Brochure**

 <p><b>Bharath</b>  <b>INSTITUTE OF HIGHER EDUCATION AND RESEARCH</b>  <small>Declared as Deemed - to - be - University under section 3 of UGC Act 1956</small>  <small>ACCREDITED WITH 'A' GRADE BY NAAC</small></p> <p>National Seminar      On      Electronics System Design      (03-08-2017)</p>  <p>Organized by ECE department      School of Engineering      BHARATH INSTITUTE OF HIGHER      EDUCATION      AND RESEARCH      DECLARED AS DEEMED TO BE      UNIVERSITY</p>	<p><b>CHIEF PATRONS</b>          Dr. J. Sundeeep Anand, President          Dr. Swetha Sundeeep Anand, Managing Director</p> <p><b>PATRONS</b>          Dr. V.Kanagasabai, Vice Chancellor          Dr. S. Bhuminathan, Registrar          Dr. J. Hameed Hussain, Dean Engineering</p> <p><b>CONVENER</b>          Dr.M.Sangeetha HOD/ECE, BIHER</p> <p><b>ORGANISING SECRETARY</b>          Ms. S.Philomina, Asst.Prof-ECE, BIHER          Ms. S.Saravana, Asst.Prof-ECE, BIHER</p> <p><b>CO-ORDINATORS</b>          Ms.S.Arulsevi, Asst.Prof-ECE, BIHER          Ms.K.Subbulakshmi Asst.Prof-ECE, BIHER</p> <p><b>TECHNICAL COMMITTEE</b>          Mr.Mohanraj, Asst.Prof-ECE, BIHER          Mr.S.Rajesh, Asst.Prof-ECE, BIHER          Ms.Sowmiya Manoj, Asst.Prof-ECE, BIHER          Mr.R.S.Sidharthraj, Asst.Prof-ECE, BIHER          Dr.B.Karthik, Assoc.Prof-ECE, BIHER          Ms..S.Arulsevi, Assoc.Prof-ECE, BIHER          Ms.G.Kanagavalli, Asst.Prof-ECE, BIHER          Ms.G.Angelovirgin, Asst.Prof-ECE, BIHER          Ms.R.Hema, Asst.Prof-ECE, BIHER          Ms.B.Hemalatha, Asst.Prof-ECE, BIHER</p>	<p style="text-align: center;"><b>REGISTRATION FORM</b></p> <p style="text-align: center;">National Seminar      On      Electronics System Design      August 03<sup>rd</sup>, 2017</p> <p><b>Organized by ECE department</b></p> <p>Name :.....          Designation :.....          Institution:.....          Mailing address:.....          Mobile:.....          Email:.....          Title of the paper:.....          DD Nu./Date:.....          Drawn on bank.....</p> <p>Date: _____ Signature of the applicant :</p> <p>Forwarded through Head of the Institute</p> <p>For any query, please contact the below mail id:          arulsevi2003@gmail.com</p>
---	--	--

## ABOUT THE INSTITUTION

Bharath Institute of Science and Technology (BIST) was the first self financing Engineering College established in Tamil Nadu in 1984. On the recommendation of University Grants Commission (UGC), Bharath Institute of Higher Education and Research (BIHER) was declared as deemed to be university by Ministry of Human Resource and Development (MHRD), Government of India, in the year 2003. Bharath Institute of Science and Technology (BIST), a constitute college of BIHER conducts various Academic and Research Programmes in Engineering & Technology, Architecture and Management studies. BIST has a state of art facilities for Undergraduate, Postgraduate and Research Programmes. The students of BIST spread all over the globe, excelling themselves in their own domains. BIHER has accredited with "A" grade by National Assessment and Accreditation Council (NAAC), India and Ranked as No. 2 for Outreach and Inclusivity in India.

## ABOUT THE DEPARTMENT

Electronics and Communication Engineering (ECE) is a swiftly advancing field, with new ideas emerging every other seconds. Graduate engineers in this discipline will be equipped to design and fabricate, install, operate and maintain complex electronic circuits, equipments and systems. The course also covers designing security in communications, besides all the software and hardware required in the communication domain.

### GUIDELINES

Abstract submission : 21-July 2017  
Notification of acceptance : 23-July 2017  
Submission of full length paper : 29-July 2017  
Last date for Registration : 01- August 2017  
Date of Seminar : 03-August 2017  
Authors are requested to submit their paper in MS-word format to the guidelines. Times New Roman, Font size 12, on A-4 Paper Size. The paper should contain a brief abstract (maximum of 200 words), 5 key words, and complete mailing address of the authors. Selected papers will be published in book format with ISBN number

## ABOUT THE SEMINAR

The objective of the seminar is to increase the awareness on Electronic System Design in the VLSI and Embedded Systems communities, especially in academic circles. This workshop also aimed to encourage teaching Electronic System Design as a subject in undergraduate and postgraduate levels and promote research in related areas.

The workshop was needed because in India, Electronic System Design is being practiced in industry, but the emphasis in academics on the same has been very limited. Bridging this gap is essential. This Seminar was an attempt to provide and opportunity for the academicians and students to improve their familiarity with and increase their interest in the Electronic System Design area. The forum that can be created through this seminar also can help promote research inputs from academics to industry.



## SCHOOL OF ELECTRICAL ENGINEERING

### Seminar on Electronic System Design

### LIST OF PARTICIPANTS

**TOTAL NO. OF PARTICIPANTS: 55**

S.NO	NAME OF THE PARTICIPANT	CATEGORY	INSTITUTE	CITY	STATE
1	ANNA MALLA ICT	STUDENTS	BHARATH UNIVERSITY	CHENNAI	TAMILNADU
2	MOTHE SWARANG	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	CHENNAI	TAMILNADU
3	PINJARI REHMAN VALIBASHA	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	SECUNDRABAD	TELEGANA
4	HARI HARANM	STUDENTS	BHARATH UNIVERSITY	HYDERABAD	TELEGANA
5	POTLADUR THISREE NIVASATEJA	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	HYDERABAD	TELEGANA
6	PRIYANGA AT	STUDENTS	BHARATH UNIVERSITY	NELLORE	ANDRAPRADESH
7	THIMMIGARISAIVARDHAN	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	THIRUMALAPUR	TELEGANA
8	BORAGANESH	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	NELLORE	ANDRAPRADESH
9	MUNTA PRAVEEN KUMAR	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	THIRUMALAPUR	TELEGANA
10	MATHI UKHONANG	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	HYDERABAD	TELEGANA
11	SANGAVIB	STUDENTS	BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH	SECUNDRABAD	TELEGANA

12	ANCHAKRISHNARE DDY	STUDENTS	BIHER	MACHERIAL	TELEGANA
13	BUGUDIRAMU	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	HYDERABAD	TELEGANA
14	GANJIBHARATHCH ANDRA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	CHENNAI	TAMILNADU
15	KATIKERIVENKATES H	STUDENTS	BHARATH UNIVERSITY	NELLORE	ANDRAPRADESH
16	KOWSALYAK	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	CHENNAI	TAMILNADU
17	UNNAMSIVAKRISH NA	STUDENTS	BHARATH UNIVERSITY	NELLORE	ANDRAPRADESH
18	RAUSHANKUMAR	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	MADHUBANI	BIHAR
19	ALIVELICHARANSAI	STUDENTS	BIHER	CHENNAI	TAMILNADU
20	BASIMALLAMARKR ATNABOSE	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	HYDERABAD	TELEGANA
21	THATIKONDAPREM SAI	STUDENTS	BHARATH UNIVERSITY	SECUNDARAB AD	TELEGANA
22	DASINENIANUSHA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	HYDERABAD	TELEGANA
23	KOLLUMALLASHYA MNAGASRIKIRAN	STUDENTS	BIHER	CHENNAI	TAMILNADU
24	SUNKURAMNAVICH AND	STUDENTS	BIHER	MACHERAIL	TELEGANA
25	JPHIMAVANTH	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	PATNA	BIHAR
26	THIRIVEEDISUBBA MMA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	CHENNAI	TAMILNADU
27	PEMMASANIKAVYA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH	ONGOLE	ANDRAPRADESH
28	KAMASANIYOTHIP RAKASHREDDY	STUDENTS	BIHER	NELLORE	ANDRAPRADESH
29	PEDINEEDINAGARJ UNA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND RESEARCH		ANDRAPRADESH
30	YANAMA/LASREEK ANTHREDDY	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND	HYDERABAD	TELEGANA

			DRESEARCH		
31	MOTEKEERTHI	STUDENTS	BIHER	SECUNDARABAD	TELEGANA
32	ADDALAKRISHNAV AMSI	STUDENTS	BIHER	MADHUBANI	BIHAR
33	GOKARAJUJAYBABU	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	NELLORE	ANDRAPRADESH
34	MANECHANDRAKANTH	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	MANCHERIAL	TELEGANA
35	SANNUTHIVINAYNA GAPRANEETH	STUDENTS	BIHER	MANTHANI	TELEGANA
36	TELLAMEKALAPRADEEPKUMAR	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	ARMOR	TELEGANA
37	MATTAPALLISAIRI KANTHYADAV	STUDENTS	BHARATHUNIVERSITY	VIJAYAWADA	ANDRAPRADESH
38	PULINAGANATHREDDY	STUDENTS	BIHER	CHENGALPAT	TAMILNADU
39	KOTTRAANUSHAGUD	STUDENTS	BIHER	CHENNAI	TAMILNADU
40	KRADHAKRISHNA	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	CHENNAI	TAMILNADU
41	KAPULURIMARUTHIRAO	STUDENTS	BIHER	THIRUMALAPUR	TELEGANA
42	RAMPRASATH	STUDENTS	BIHER	HYDERABAD	TELEGANA
43	KAPULURIVENKATESWARLU	STUDENTS	BIHER	CHENNAI	TAMILNADU
44	MUNGARASRIHARI BABU	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	SECUNDARABAD	TELEGANA
45	MANNEMANOJKUMARREDDY	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	VIJAYAWADA	ANDRAPRADESH
46	MARUBOYINASANDEEP	STUDENTS	BHARATHUNIVERSITY	NELLORE	ANDRAPRADESH
47	MDF AISALALAM	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	CHENNAI	TAMILNADU
48	MDSHAHRUKHALAM	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	HYDERABAD	TELEGANA
49	MUKUNTHANR	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND	HYDERABAD	TELEGANA

			DRESEARCH		
50	MUPPALAAKHIL	STUDENTS	BHARATHUNIVERSITY	THIRUMALAPUR	TELEGANA
51	MUPPALAPRANEETH	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	ARMOOR	TELEGANA
52	MUTHURAJM	STUDENTS	BIHER	NELLORE	ANDRAPRADESH
53	NADARRAHINITHA NASEELAN	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	RAJAHMUNDRI	ANDRAPRADESH
54	NAIDULOKESH	STUDENTS	BHARATHINSTITUTE OF HIGHER EDUCATION AND DRESEARCH	KUNDURU	ANDRAPRADESH
55	NALAGATLAMOURY ACHAKRAVARTHYC HOWDARY	STUDENTS	BIHER	NELLORE	ANDRAPRADESH



**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**  
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

## SCHOOL OF ELECTRICAL ENGINEERING

Seminar on Electronic System Design dated on 03-08-2017 conducted by School of Electrical Engineering





**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**  
No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

## SCHOOL OF ELECTRICAL ENGINEERING

Seminar on Electronic System Design

### CERTIFICATE



**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)  
ACCREDITED WITH 'A' GRADE BY NAAC

SCHOOL OF ELECTRICAL ENGINEERING

### CERTIFICATE OF PARTICIPATION

This is to certify that Mr/Ms M.O.T.E..K.E.E.R.T.H.I.....has attended  
**Seminar on Electronic System Design** Organized by the Department of  
Electronics and Communication Engineering, BIHER conducted from 03<sup>rd</sup>  
August, 2017

*S. Arulselvi*

Dr. S.Arulselvi Ms. K.Subbulakshmi  
COORDINATOR

*M. Sangeetha*

Dr. M.Sangeetha  
CONVENOR



**SCHOOL OF ELECTRICAL ENGINEERING**

**Seminar on Electronic System Design**

FEED BACK FORM		Date: 17.08.2017			
Name	Harisharan.M				
Phone number	9557501823				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
Seminar's objective were stated clearly and met?			✓		
Audio, Visual Aids Technology used			✓		
Presentation hand outs				✓	
Seminar enhances your Studies/Research?			✓		
The information presented was useful?				✓	
The speaker was engaging and Knowledgeable?			✓		

  
Student Signature