



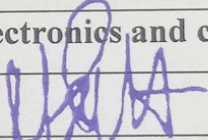
Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956]


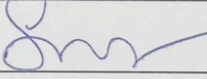
Chennai – 600 073

INTERNAL QUALITY ASSURANCE CELL (IQAC)

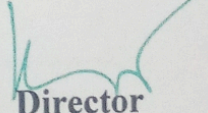
DOCUMENTS SUBMISSION FORM

Date of Submission	12.11.2017
Type of Documents	Seminar
Description	Seminar on ORCAD for Circuit Simulation and PCB
Enclosures	a) Requisition Letter
	b) Circular
	c) Schedule
	d) Certificate
	e) Feedback form
	f) Photos
	g) Invitation
No. of Pages	14
Submitted By	Name : Dr.M.Sangeetha
	Designation: Professor & Head
	Department: Electronics and communication Engineering
	Signature : 

For Office Use Only

Verified By:	K. Sakthivel	Sign: 	Date: 12/11/2017
Uploaded By:	K. Senthil Kumar	Sign: 	Date: 12/11/2017
File Name:	SEMO - BEE - ECE - 2017 - 2018 - 15		




Director
IQAC – BIHER



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(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

REQUISITION LETTER

06.08.2017

FROM

The HOD

Department of ECE

Bharath Institute of Higher Education and Research

Selaiyur, Chennai-73

TO

The Dean Engineering

Bharath Institute of Higher Education and Research

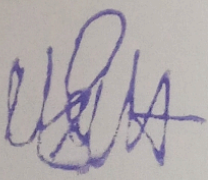
Selaiyur, Chennai-73

Respected Sir,

Subject: Requisition for conducting a seminar on "ORCAD for Circuit Simulation and PCB" - Regarding.

The School of Electrical Engineering has planned to conduct a Seminar on the topic "ORCAD for Circuit Simulation and PCB" dated on 07.11.2017 between 9.30 PM to 4.30 PM. In this regard, I request you to kindly grant permission for conducting the same.

Thanking You


HOD/ECE


Dean Engineering



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No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

07.08.2017

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER.

To
B.Ravikumar,
Senior Manager,
IBM Pvt. Ltd,
Bangalore – 636001.

Respected sir,

Sub:-The Department of Electronics and Communication Engineering, School of Electrical Science, BIHER, proposed to conduct a seminar in “ORCAD for Circuit Simulation and PCB ”- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “ORCAD for Circuit Simulation and PCB “in association with IBM Pvt. Ltd, Bangalore, on 07.11.2017.Kindly accept our request and do the needful

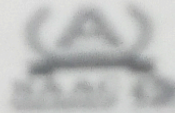
Convener

Dr.M.Sangeetha, HOD/ECE



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH
BANGALORE - 560075



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No. 175, Rajahmundry Road, Bangalore, Karnataka - 560075

DEPARTMENT OF ECE

Program Schedule

07/11/2015 (Tuesday)

09.30 - 10.00 AM	Inaugural Address
09.30 AM	Welcome Address Dr. M. Sangeetha
09.35 AM	About the Seminar Ms. C. Jayalakshmi
09.40 AM	Introduction of the Chief Guest Dr. S. Aravindan
09.45 AM	Inaugural address by Chief Guest R. Ravikumar, Senior Manager, IBM Pvt. Ltd, Bangalore - 430001
10.00 AM Onwards	Invited Talk R. Ravikumar, Senior Manager, IBM Pvt. Ltd, Bangalore - 430001
12.30 - 1.30 PM	Lunch break
1.45 - 03.00pm	Presentation
	<u>Validictory program</u>
03.30 - 04.00PM	Validictory Function
04.00 - 04.20PM	Vote of Thanks (Coordinator)



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ESTABLISHED IN THE YEAR 1983



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

NO. 175, Agthavani Road, Saravali, Chennai - 600 075

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 08.08.2017

The School of Electrical Engineering, Department of ECE, BIHER is glad to conduct a Seminar on "ORCAD for Circuit Simulation and PCB" dated on 07.11.2017 in association with IBM, Pvt. Ltd, Bangalore, between 9.30AM to 4.30PM. Those who are interested to participate do register your name with the program coordinator mentioned below.

Resource Person:

B. Ravikumar,

Senior Manager,

IBM Pvt. Ltd,

Bangalore - 636001

Convener:

Dr. M. Sangeetha,

HOD/ECE

Program Coordinator:

Mrs. G. Jeyalakshmi

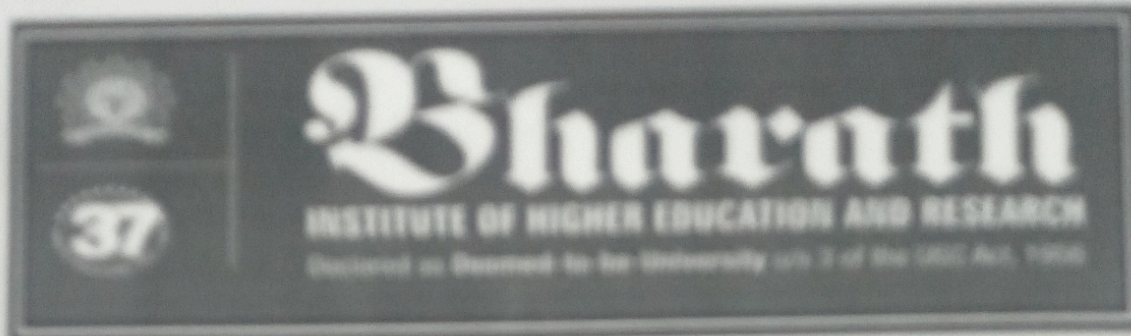
Assistant Professor

Email: jeyalakshmi99@gmail.com

Contact no: 9840976989 Convener

Dr. M. Sangeetha, HOD/ECE

INVITATION



School of Electrical Engineering

Department of Electronics and Communication Engineering

Seminar on

"ORCAD for Circuit Simulation and PCB"

Resource Person : B. Ravikumar, Senior Manager, IBM Pvt. Ltd,
Bangalore - 636001.

KEYNOTE ADDRESS : Dr. J. Hameed Hussain, Dean Engineering, BIHER.

SPECIAL ADDRESS : Dr. M. Sangeetha, Professor/HOD, ECE, BIHER

PROGRAM COORDINATOR : Ma. G. Jayalalitha, Assistant Professor, ECE, BIHER

VENUE : ECE SMART ROOM, SA BLOCK, BIHER.

DATE : 07.11.2017

TIME : 9.30 A.M - 4.30 PM

Registration form:

Name :

Designation:

Name of the Title :

Area of Research :

Organization :

Address for Communication :

Contact No:

Gmail :

Category : PG

students/Research

Scholar/Faculty/Industrial
person

Payment Details :

D.D No/ Date :

Drawn on Bank :

Amount :

Signature Head of Institution

Date :

(Photo copies of the form
accepted)

Advisory Committee:

Dr.J.Sundeep Anand

Chancellor/BIHER

Dr.S.Sweetha Anand

Managing Director/BIHER

Dr.V.Kanagasabai

Vice chancellor/BIHER

Co-Patron

Dr.K.P.Thooyamani

Pro vice chancellor/BIHER

Dr.J.Hameed Hussian

Dean Engineering /BIHER

Dr.M.Sundararajan

Dean School of Electrical
science/BIHER

Convener

Dr.M.Sangeetha

Professor & HOD/ECE

Program Committee:

Dr S.Arulselvi

Associate Professor/ECE

Technical Committee:

Dr.S.Philomina

Assistant Professor/ECE

G.Jeyalakshmi

Assistant Professor/ECE

Organizing Secretary:

S. Sandeep

Student of ECE

K.Ajay

Student of ECE



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BHARATH INSTITUTE OF HIGHER
EDUCATION AND RESEARCH

173, Aghram road, Selaiyur,
Chennai-600073

National Seminar
on
**“ORCAD for Circuit
Simulation and PCB”**

07th November 2017

Organized
by

DEPARTMENT OF ELECTRONICS
AND COMMUNICATION
ENGINEERING

In association with
IBM, Private Ltd



www.bharathuniv.ac.in

About the University:

The first self-financing Engineering College in the name of Bharath Institute of Science and Technology (BIST) was started in Tamil Nadu in 1984 by Sri Lakshmi Ammal Educational Trust, established by the academic visionary Dr.S.Jagathrakshakan

Bharath University and is known for providing high-quality education to around 10000 students from across the world in multi-stream. The institute facilitates the guidance of 1300 teaching faculty members to provide guidance to do Undergraduate, Postgraduate programs and to facilitate Ph.D. research.

About the Department:

Electronics and Communication Engineering (ECE) is a swiftly advancing field, with new ideas emerging every other seconds. Graduate engineers in this discipline will be equipped to design and fabricate, install, operate and maintain complex electronic circuits, equipment's and systems.

The course also covers designing security in communications, besides all the software and hardware required in the communication domain.

About the Seminar:

Orcad is an open source computational software used for assembling components on PCB board and use defined subroutines for solving complex mathematical problems. This seminar will help participants develop good understanding of computational software and have good hands on experience on how to assemble the different engineering problems using the software. The main focus of the course is on imparting the importance of using such tools and as well making the participants familiar with industry grade problems and solutions in engineering.

Seminar Structure:

Theory of Orcad - Get familiar with

1. Different Programming Languages available at disposal
2. What is Orcad
3. Usage of Orcad in industry
4. Different libraries available in Orcad
5. Syntax in Orcad
6. Developing and compiling a sub-routine with PCB.
7. Limitations of using Orcad
8. Common errors while using Orcad

We cordially invite the students, research scholars, Faculties, Industrial persons to explore the various areas of research and to enhance research skill and to enlighten those around you.

Practical Session - Gain hands-on experience by

1. Developing simple sub-routines and compiling them
2. Using in built libraries to solve common engineering problems
3. Exploring different methods of visualizing the outcome (PCB Layout, etc)
4. Solving industry related engineering problems and devising solutions

Brainstorming session - Solve an assigned problem covering the entire topics on your own at the venue to assess your learning.

The Research scholars and students, Faculties can confirm their registration by sending the DD. The DD should take in favour of "BIHER Chennai" before 15.10.2017, backside of DD carry full name, contact number and address of the author.

Selected candidates list announced through email. Accommodation shall be arranged on request at a reasonable cost.

Dates to be Remember :

Last date for Registration: 30/10/2017

Short Listed candidates list announcement: 05/11/2017

Registration fee details:

Students	: Rs.250/-
Research scholars & Staff members	: Rs.500/-
Industry Person	: Rs.750/-

Bharath Institute of Higher Education and Research
Department of ECE
PARTICIPANTS LIST
Total No. Of Students=40

S.NO.	NAME OF THECANDIDATE	Name of the Department & College
1	U14EC001 - AARTHI.P	ECE, Bharath Institute of Higher Education and Research
2	U14EC005 - AMARJEET KUMAR	ECE, Bharath Institute of Higher Education and Research
3	U14EC009 - ANNAPANENI VAMSIDHAR	ECE, Bharath Institute of Higher Education and Research
4	U14EC010 - ARCHANA.R	ECE, Bharath Institute of Higher Education and Research
5	U14EC015 - CHANDRALEKA.K	ECE, Bharath Institute of Higher Education and Research
6	U14EC020 - CHINTA ANVESH	ECE, Bharath Institute of Higher Education and Research
7	U14EC0 30 - DEBAJIT HAZARIKA	ECE, Bharath Institute of Higher Education and Research
8	U14EC035 - GOKUL.S	ECE, Bharath Institute of Higher Education and Research
9	U14EC046 - GUNDA VENKATA MEHAR VINAY	ECE, Bharath Institute of Higher Education and Research
10	U14EC052 - GUTTA MOHAN RANGA RAO	ECE, Bharath Institute of Higher Education and Research
11	HARSHA VARDHAN .K	ECE, Sri Venkateshwara Engineering and Technology, Chennai
12	INDU GOPI.R	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
13	KRISHNA.S	ECE, Sai Ram College of Engineering and Technology, Chennai
14	S. VEERA LAKHSMI	ECE, Sri Venkateshwara Engineering and Technology, Chennai
15	SURAJ SAH	ECE, Sri Venkateshwara Engineering and Technology, Chennai
16	SABITA KUMARI	ECE, Sri Venkateshwara Engineering and Technology, Chennai
17	M.MANIKANTA	ECE, Sri Venkateshwara Engineering and Technology, Chennai
18	M. RAJ KUMAR	ECE, Sri Venkateshwara Engineering and Technology, Chennai
19	MD.FAIYAZ ALAM	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
20	MOHAMED KASHIFUDDIN.B	ECE, Dhanalakshmi college of Engineering and Technology, Chennai

21	S. KARTHIK	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
22	SANKIRTH KUMAR.B	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
23	NARESH .I	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
24	PALAPARTHI RAMBABU.E	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
25	P.VIJAYA BHARGAVI	ECE, Sai Ram College of Engineering and Technology, Chennai
26	I. BHARATHI	ECE, Sai Ram College of Engineering and Technology, Chennai
27	A.MANOJ DEEP	ECE, Sai Ram College of Engineering and Technology, Chennai
28	PRANAY KUMAR.G	ECE, Sai Ram College of Engineering and Technology, Chennai
29	T. ANUSHA	ECE, Sai Ram College of Engineering and Technology, Chennai
30	SOWMYA .V	ECE, Sai Ram College of Engineering and Technology, Chennai
31	SAI MOHAN.R	ECE, Sai Ram College of Engineering and Technology, Chennai
32	K. AKHIL	ECE, Sai Ram College of Engineering and Technology, Chennai
33	N. KUMAR	ECE, Sai Ram College of Engineering and Technology, Chennai
34	SRILATHA.L	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
35	SWETHAA SHREE.S	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
36	THILLAI VANI.S	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
37	UMA MAHEEWARY.L	ECE, Sri Venkateshwara Engineering and Technology, Chennai
38	SAHITHYA.L	ECE, Sri Venkateshwara Engineering and Technology, Chennai
39	VENKATESH BABU.B	ECE, Sri Venkateshwara Engineering and Technology, Chennai
40	S. ANAND	ECE, Sri Venkateshwara Engineering and Technology, Chennai

Bharath Institute of Higher Education and Research
Department of ECE
One day Seminar on “ORCAD for Circuit Simulation and PCB”
07.11.2017





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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
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Bharath Institute of Higher Education and Research
Department of ECE
CERTIFICATE



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SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr. / Ms ANUSHA.R (U14EC015)

has attended *Seminar on "ORCAD for Circuit Simulation and PCB"* organized
by the School of Electrical Engineering, BIHER conducted on
07/11/2017.

G.JEYALAKSHMI
COORDINATOR

Dr.M. SANGEETHA
CONVENOR



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SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
SEMINAR ON ORCAD for Circuit Simulation and PCB

FEED BACK FORM		Date: 07.11.2017			
Name	N. KUMAR				
Phonenumber	989 24 73 821				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
Seminar's objective were stated clearly and met?			✓		
Audio, Visual Aids Technology used				✓	
Presentation hand outs			✓		
Seminar enhances your Studies/Research?					✓
The information presented was useful?			✓		
The speaker was engaging and Knowledgeable?					✓

Kumar.
Student Signature



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SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
SEMINAR ON ORCAD FOR CIRCUIT SIMULATION AND PCB

FEED BACK FORM		Date:07.11.2017			
Name	Anusha				
Phonenumber	8312245734				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
Seminar's objective were stated clearly and met?					✓
Audio, Visual Aids Technology used				✓	
Presentation hand outs			✓		
Seminar enhances your Studies/Research?				✓	
The information presented was useful?					✓
The speaker was engaging and Knowledgeable?				✓	

Anusha
Student Signature