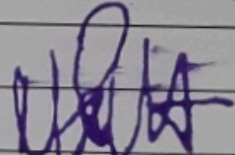





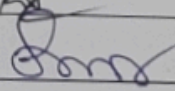
Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073.

INTERNAL QUALITY ASSURANCE CELL (IQAC)

Date of Submission	11.02.2018
Type of Documents	Seminar
Description	Seminar on System on chip design challenges
Enclosures	a) REQUISITION LETTER
	b) CIRCULAR
	c) BROCHURE
	d) LIST OF PARTICIPANTS
	e) PHOTOS
	f) CERTIFICATE
	g) FEEDBACK FORM
No. of Pages	
Submitted By	Name : Dr. M.Sangeetha
	Designation : Professor &HOD/ECE
	Department : ECE
	Signature : 

For Office Use Only

Verified By:	K. Sakthivel	Sign: 	Date: 11/02/2018
Uploaded By:	K. Sakthivel Kumal	Sign: 	Date: 11/02/2018
File Name:	SEM7 - SEE - ECE - 2017-2018 - 17		


Director IQAC – BIHER





REQUISITION LETTER

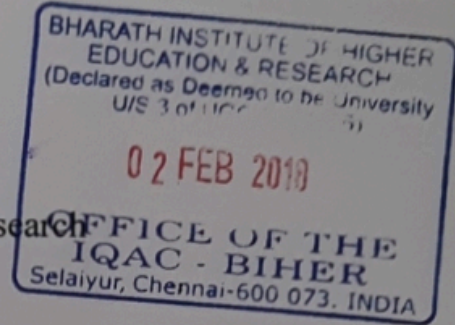
11.01.2018

FROM

The HOD
Department of ECE
Bharath Institute of Higher Education and Research
Selaiyur, Chennai-73

TO

The Dean Engineering
Bharath Institute of Higher Education and Research
Selaiyur, Chennai-73
Respected Sir,



Subject : Requisition for Conducting a seminar System on chip

design challenges -reg

The School of Electrical Engineering has planned to conduct a Seminar on the topic “**System on chip design challenges**” dated on 23.01.2018 between 1.30 PM to 4.30 PM. In this regard, I request you to kindly grant permission for conducting the same.

Thanking You

HOD/ECE

Dean Engineering



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

From
The HOD,
Department of ECE,
School of Electrical Sciences,
BIHER.

To
Mr.N.SRINIVASAN, Trimak pvt Ltd provides

Respected sir,

Sub:- The Department of Electronics and Communication Engineering, School of Electrical Sciences, BIHER, proposed to conduct a seminar in “ **System on chip design challenges**”- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “**System on chip design challenges** “in association with Indus Auto Training centre , on 19.01.2018.Kindly accept our request and do the needful.

Convener

Dr.M.Sangeetha, HOD/ECE



DEPARTMENT OF ECE

Program Schedule

19/01/2018(Friday)

09.30 - 10.00 AM	Inaugural Address
09.30 AM	Welcome Address Dr.M.Sangeetha
09.35 AM	Introduction about the Seminar B.Kalaiselvi
09.40AM	Introduction of the Chief Guest Dr.E.Kanniga
09.45 AM	Inaugural address by Chief Guest Mr.N.SRINIVASAN, Trimak pvt Ltd provides
10.00 AM Onwards	Invited Talk Mr.N.SRINIVASAN
12.30-1.30 PM	Lunch break
1.45-03.00pm	Presentation

Valedictory program

03.30-04.00PM	Valedictory Function
04.00-04.20PM	Vote of Thanks (Coordinator)



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selayur, Chennai , T.N - 600 073.

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 03.01.2018

The School of Electrical Sciences, Department of ECE, BIHER is glad to conduct a Seminar on “Skill development program and GATE” dated on 19.01.2018 between 1.30PM to 4.30PM. Those who are interested to participate do register your name with the program coordinator mentioned below.

Resource Person:

Mr.N.SRINIVASAN, Trimak pvt Ltd provides

Convener:

Dr.M.Sangeetha,

HOD/ECE

Program Coordinator:

Ms.B Kalaiselvi

Assistant Professor

Email:Kalaiselvi.ece@bharathuniv.ac.in

Contact no:9123505079

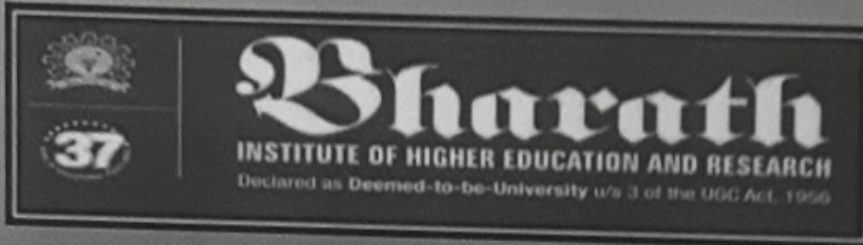
Convener

Dr.M.Sangeetha, HOD/ECE

Bharath Institute of Higher Education and Research

Department of ECE

Invitation



SCHOOL OF ELECTRICAL SCIENCES

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SEMINAR

ON

Seminar on System on chip design challenges

RESOURCES PERSON'S: **N.SRINIVASAN, Trimak pvt Ltd**
provides Industrial Automation training and Service Provider

KEYNOTE ADDRESS: **Dr.J. Hameed Hussain**
Dean Engineering, BIHER, Chennai

SPECIAL ADDRESS: **Dr.M. Sangeetha**
Head of the Department
ECE, BIHER

VENUE: **ECE SMARTROOM**
SA BLOCK, BIHER

DATE: **23.01.2018**

TIME: **01.30PM TO 4.30PM**

Registration Form

Name :

Designation :

Title of Paper :

Area of Research :

Organization :

Address for Communication:

Contact No :

Email :

Category: PG Student/Research Scholar
Faculty/Industry

Payment Details :

D.D.No :

Date:

Drawn on Bank :

Amount:

Signature

Head of Institution

Date:

(Photocopies of the form accepted)

Advisory Committee:

Dr.J.Sundeep Aanand

Chancellor, BIHER

Dr.Swetha Sundeep Aanand

Managing Director, BIHER

Dr.V. Kanagasabai

Vice Chancellor, BIHER

Co-Patron

Dr.K.P.Thooyamani

Pro Vice-Chancellor, BIHER

Dr.J.Hameed Hussian

Dean Engineering, BIHER

Dr.M.Sundarrajan.

Dean School of Electrical Science

Engineering, BIHER

Convener

Dr.M.Sangeetha Prof & Head/ ,

Program Committee:

Mrs.Kalaiselvi B/ BCE

Technical Committee:

Mr.Vijayam T / ECE

Mr.Siddharth / ECE

Organizing Secretary:

Dr.E.Kanniga / ECE

Mr. Gangeseti Sai teja Student of ECE



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INSTITUTE OF HIGHER EDUCATION AND RESEARCH
SCHOOL OF ELECTRICAL SCIENCES

**BHARATH INSTITUTE OF HIGHER
EDUCATION AND RESEARCH**

173, Agaram road, Selalger, Chennai-600078

NATIONAL SEMINAR

ON

System on chip design challenges

23rd January 2018

ORGANIZED

BY

DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING

SCHOOL OF ELECTRICAL SCIENCES



www.bharathuniv.ac.in

About the University

The National Conference is being organized by the Department of Electronics and instrumentation engineering.

Bharath Institute of Science and Technology was the first self-financing Engineering College in Tamilnadu established in 1984. It is a role model for self-financing engineering colleges. It has grown from strength to strength and attained the University status in the year 2002. Now it is running many UG, PG and Ph. D Programmes. The college has industry interaction with National and International Organizations. Recently the University was awarded as "Best Multi Stream University" at National Education Excellence Awards 2014.

About the Departments:

The departments share the mission and vision of the institute in imparting high-quality education to the students. The departments have highly qualified, experienced committed and research-oriented faculty members.

The department offers B. Tech degree Programme and M. Tech degree Programme in Electronics and Communication Engineering.

Research work is being carried out in the advanced areas of Wireless control telecommunication and Networking. This conference is aimed for creating forum for researchers, academicians, and industry people to explore emerging trends and innovative technologies.

About the Seminar

The technical feat of integrating this separate element is enormous. It requires development of compatible process technologies, interface bus architectures, and interoperable hardware/software verification tools. Integration is also a challenge because of the demands placed on technology and design at the deep sub-micron level. Challenges of deep sub-micron design include.

Thrust Area:

1. ASIC Design
2. CMOS VLSI Design
3. VLSI Design
4. Analog IC design
5. MOSFETS design

We cordially invite the students research Scholars, Faculties, industrial persons to explore various areas of research and to enhance research and to enhance research skill and to enlighten those around you.

Instructions for the Participants:

Those who want to participate in this National level Seminar fill their details in the registration form and get signature from their Higher Authorities. Along with the registration form take DD drawn in favour of "Dean Engineering" BIHER payable at Punjab National Bank (PNB), selaiyur, branch - Chennai-73, should be sent to the Head of Dept. of ECE/BIHER, for their participation confirmation.

DD should carry full name, contact number and address of the participants.

Note: Accommodation shall be arranged on request basis at a reasonable cost.

Important Dates:

Submission of Abstract: Jan 19th, 2018

Submission of full paper: Jan 21st, 2018

Registration Fee details:

Authors

Students: 250/-

Research Scholars/ Teaching Professional: 500/-

Industry Delegates: 750/-

Address for Correspondence

The Conveners,

Department of ECE

BIHER

173, Agaram road, Selaiyur, Chennai-600073

Phone: 044-22290125, 22293886

E-mail: ncwes'18@gmail.com

www.bharathuniv.ac.in

Think ahead. Stay ahead.

Bharath Institute of Higher Education and Research

Department of ECE

PARTICIPANTS LIST

S.NO.	NAME OF THE CANDIDATE	Name of the Department & College
1	BHUPATHI VENKATASAI UMA SANKAR .	ECE, Bharath Institute of Higher Education and Research
2	BALAM HIMAJA	ECE, Bharath Institute of Higher Education and Research
3	CHAMARTHI REDDYSEKHAR RAJU .	ECE, Bharath Institute of Higher Education and Research
4	THATHIREDDY MAHESH	ECE, Bharath Institute of Higher Education and Research
5	CHANDRAGIRI MALLIKARJUNAREDDY	ECE, Bharath Institute of Higher Education and Research
6	GARYALA PHANEENDRA KUMAR .	ECE, Bharath Institute of Higher Education and Research
7	PULAGAM NAVEEN KUMAR REDDY .	ECE, Bharath Institute of Higher Education and Research
8	KANJULA HRUSHIEKESH REDDY .	ECE, Bharath Institute of Higher Education and Research
9	KAREDLA VINAY VARDHAN REDDY .	ECE, Bharath Institute of Higher Education and Research
10	VENNAPUSA MAHESWAR REDDY .	ECE, Bharath Institute of Higher Education and Research
11	MUDDAM AJAYKUMAR .	ECE, Bharath Institute of Higher Education and Research
12	DUMPA GANESH	ECE, Bharath Institute of Higher Education and Research

13	VYSYARAJU SIVARAM	ECE, Bharath Institute of Higher Education and Research
14	THANGELLA AJAY KUMAR .	ECE, Bharath Institute of Higher Education and Research
15	PANABAKAM LEKHASREE	ECE, Bharath Institute of Higher Education and Research
17	SOWMYA .V	ECE, Sai Ram College of Engineering and Technology, Chennai
18	SAI MOHAN.R	ECE, Sai Ram College of Engineering and Technology, Chennai
19	K. AKHIL	ECE, Sai Ram College of Engineering and Technology, Chennai
20	N. KUMAR	ECE, Sai Ram College of Engineering and Technology, Chennai
21	SRILATHA.L	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
22	SWETHAA SHREE.S	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
23	THILLAI VANI.S	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
24	M. RAJ KUMAR	ECE, Sri Venkateshwara Engineering and Technology, Chennai
25	MD.FAIYAZ ALAM	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
26	MOHAMED KASHIFUDDIN.B	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
27	S. KARTHIK	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
28	SANKIRTH KUMAR.B	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
29	NARESH .I	ECE, Dhanalakshmi college of Engineering and Technology, Chennai

30	PALAPARTHI RAMBABU.E	ECE, Dhanalakshmi college of Engineering and Technology, Chennai
31	P.VIJAYA BHARGAVI	ECE, Sai Ram College of Engineering and Technology, Chennai
32	I. BHARATHI	ECE, Sai Ram College of Engineering and Technology, Chennai
33	A.MANOJ DEEP	ECE, Sai Ram College of Engineering and Technology, Chennai
34	PRANAY KUMAR.G	ECE, Sai Ram College of Engineering and Technology, Chennai
35	T. ANUSHA	ECE, Sai Ram College of Engineering and Technology, Chennai

Bharath Institute of Higher Education and Research
Department of ECE

PHOTOS



Bharath Institute of Higher Education and Research

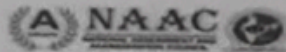
Department of ECE

CERTIFICATE



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
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


SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms MENTA PADMA RAGINI (U17EC268) has Presented/ Participated Seminar on **System on chip design challenges** organized by the School of Electrical Engineering, BIHER conducted on **23.01.2018**


Dr. E. Kanna
CO ORDINATOR


Dr. M. Sangeetha
HOD / ECE



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SCHOOL OF ELECTRICAL ENGINEERING

FEEDBACK FORM

SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES

FEED BACK FORM		Date: 19.01.2018			
Name	N. KUMAR				
Phone number	9785143090				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
Seminar's objective were stated clearly and met?					✓
Audio, Visual Aids Technology used				✓	
Presentation hand outs				✓	
Seminar enhances your Studies/Research?				✓	
The information presented was useful?					✓
The speaker was engaging and Knowledgeable?				✓	

N. Kumar

Student Signature



SCHOOL OF ELECTRICAL SCIENCES

FEEDBACK FORM

SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES

FEED BACK FORM		Date:19.01.2018				
Name	V. Sowmya					
Phone number	9789193423					
	Poor	Fair	Good	Very Good	Excellent	
Overall Program				✓		
Seminar's objective was stated clearly and met?					✓	
Audio, Visual Aids Technology used				✓		
Presentation hand outs						
Seminar enhances your Studies/Research?			✓			
The information presented was useful?				✓		
The speaker was engaging and Knowledgeable?				✓	✓	

V. Sowmya

Student Signature