



Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073

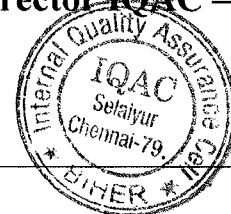
INTERNAL QUALITY ASSURANCE CELL DOCUMENT SUBMISSION FORMS

Date of Submission	30/10/2017
Type of Documents	Workshop
Description	Two days workshop on ANALOG VLSI DESIGN
Enclosures	a) Requisition Letter
	b) Permission Letter
	c) Circular
	d) Schedule
	e) Invitation
	f) Brochure
	g) List of Participants
	h) Feedback
	i) Photo
	j) Certificate
No. of Pages	14
Submitted By	Name : Dr.M.Sangeetha
	Designation : Professor & Head
	Department : Electronics and communication Engineering
	Signature :

For Office Use Only

Verified By:	K. Sakthivel	Sign:	Date: 30/11/2017
Uploaded By:	K. Senthil Kumar	Sign:	Date: 30/11/2017
File Name:	WORK - SEE - ECE - 2017-2018-04		

Director IQAC – BIHER





Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

REQUISITION LETTER

02.10.2017

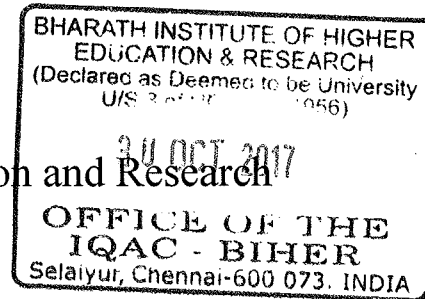
FROM

The HOD

Department of ECE

Bharath Institute of Higher Education and Research

Selaiyur, Chennai-73



TO

The Dean Engineering

Bharath Institute of Higher Education and Research

Selaiyur, Chennai-73

Respected Sir,

Subject: Requisition for Conducting Workshop -reg

The School of Electrical Engineering has planned to conduct two days workshop on the topic "Analog VLSI Design" in association with "The London Northwest Healthcare NHS Trust" dated 23.10.2017 & 24.10. 2017. In this regard, I request you to kindly grant permission for conducting the same

Thanking You

HOD/ECE

Dean Engineering



Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

04.10.2017

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER.

To
Dr.Abhishek Sharma,
Intel Crporation,
Chennai.

Respected sir,

Sub: - The Department of Electronics and Communication Engineering,
School of Electrical Engineering, BIHER, proposed to conduct a workshop on **Analog Vlsi Design** dated **23.10.2017 & 24.10. 2017**- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “**Analog VLSI Design** “in association with “The London Northwest Healthcare NHS Trust “, London on 23.10. 2017.Kindly accept our request and do the needful .

Convener

Dr.M.Sangeetha
HOD/ECE



04.10.2017

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER

To
Dr.Sunitha P.Ugale,
KKWIERR,Nasik,
Chennai

Respected sir,

Sub: - The Department of Electronics and Communication Engineering,
School of Electrical Engineering, BIHER, proposed to conduct a workshop in “**Analog Vlsi Design**” dated on **23.10.2017 & 24.10. 2017**- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in association with “The London Northwest Healthcare NHS Trust” ,London “dated on 24.10.2017. Kindly accept our request and do the needful.

Convener:

Dr.M.Sangeetha
HOD/ECE



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

CIRCULAR SCHOOL OF ELECTRICAL ENGINEERING

Date: 16.10.2017

The Workshop on "Analog VLSI Design" is planned by the School of Electrical Engineering on 23.10.2017 & 24.10.2017.

This Workshop Programme is offered by the Department Of ECE, Bharath Institute of Higher Education and Research, for the duration of two days (23.10.2017 & 24.10.2017).

In this Regard, Students are instructed to give their willingness and confirm their participation to their respective class in charge/workshop Coordinator before 19.10.2017.

Ms.G.Jeyalakshmui, Ms.B.Kalai Selvi

Assistant Professor/ECE, School of electrical Engineering

Workshop Coordinator

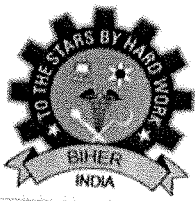
Bharath Institute of Higher Education and Research

Email id: jeyalakshmi.ece@bharathuniv.ac.in.

Convener:

Dr.M.Sangeetha,

HOD/ECE



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING WORKSHOP ON ANALOG VLSI DESIGN

SCHEDULE

23/10/2017

09.30 - 10.00 AM	Inaugural Address
09.30 AM	Welcome Address Dr.M.Sangeetha
09.35 AM	About the Workshop Ms.G,Jeyalakshmi
09.40AM	Introduction of the Chief Guest Dr. Hameed Hussain
09.45 AM	Inaugural address by Chief Guest Dr. Abhishek Sharma Intel Corporation Chennai
10.00 AM Onwards	Invited Talk Dr. Abhishek Sharma Intel Corporation Chennai
12.30-1.30 PM	Lunch break
1.45-03.00pm	Session on Low Power VLSI design
<u>Valedictory program</u>	
03.30-03.45PM	Valedictory Function
03.45-04.00PM	Vote of Thanks (Coordinator)

Bharath Institute of Higher Education and Research

SCHOOL OF ELECTRICAL ENGINEERING **WORKSHOP ON ANALOG VLSI DESIGN** **SCHEDULE**

24/10/2017

09.30 AM

Welcome Address
Dr.M.Sangeetha

09.40AM

Introduction of the Chief Guest
Dr. Hameed Hussain

Dr.Sunitha.P.Ugale
KKWEIRR,Nasik.

09.30 -11.00 AM

Session on Analog Vlsi Design

11.00 AM-12.30 PM

Session on Fundamentals of CMOS OP-AMP OTA.

12.30-1.30 PM

Lunch break

1.30-2.30PM

Low Power Vlsi Design

02.30-3.15PM

Basics of RF Design.

Valedictory program

03.30-03.45PM

Valedictory Function

03.45-04.00PM

Vote of Thanks (Coordinator)



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
INVITATION



School of Electrical Engineering
Department of ECE
BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH
in association with
The London Northwest Healthcare NHS Trust

WORKSHOP ON
ANALOG VLSI DESIGN

Resource Persons: Dr. Abhishek Sharma, Intel Corporation, Chennai
Dr. Sunitha P. Ugale, KKWIERR, Nasik.

Keynote Address:

Special Address:

Program Coordinator:

Dr. J. Hameed Hussain, Dean Engineering, BIHER, Chennai
Dr. M. Sangeetha, Professor & Head/ ECE, BIHER
Mrs. G. Jeyalakshmi & Mrs. B. Kalaiselvi

Venue : Smart Room, ECE, BIHER

Date : 23.10.2017 & 24.10.2017

Time : 11.00 am to 3.00 pm



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research Department of ECE WORKSHOP ON ANALOG VLSI DESIGN

BROCHURE

REGISTRATION FORM

1. Name
2. Department
4. Designation
5. Qualification
6. Mobile Number
7. Contact Address:
8. E-Mail ID

REGISTRATION FEE: RS 750

PAYMENT DETAILS:

D.D.No: _____ Date: _____

Drawn on Bank: _____

Amount: _____

Signature _____ Head of Institution

Date: _____
(Photocopies of the form accepted)

CO-ORDINATORS:

Ms. G. Jayalakshmi, Ms. B. Kalai Selvi
Assistant Professor, ECE

VENUE:

Smart Room
Department of Electronics and
Communication Engineering, BIHER
Chennai.

COURSE CONTENT

- Analog fundamentals-CMOS-OPAAMP-OTA fundamentals
- Fundamentals of filter design
- Design methodologies such as Active RC filters, OTA-C filters, Current-mode filters, Switched capacitor filters
- Effect of non-ideal passive and active components such as tolerances of components
- Design choices for low power supply voltage designs
- PLL Design, Basics of RF CMOS design, Analog layout, Analog Fault diagnosis, SPICE analysis of various circuits

RESOURCE PERSONS

1. **Dr. Abhishek Sharma**
Intel Corporation, Chennai.

2. **Dr. Subbu P. Ugal**
KKWIEER, NASIK,

CONVENER

Dr. M. Supratha
Prof & Head/ECE



Bharath

**BHARATH INSTITUTE OF HIGHER
EDUCATION AND RESEARCH**
173, Agharam Road, Selaiyur, Chennai - 600 073

**WORKSHOP
ON
ANALOG VLSI DESIGN
23rd and 24th OCTOBER 2017**

ORGANIZED
BY
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
SCHOOL OF ELECTRICAL SCIENCES



www.bharathuniv.ac.in



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE

WORKSHOP ON ANALOG VLSI DESIGN

PARTICIPANTS LIST

TOTAL NUMBER OF PARTICIPANTS:40

S.NO	STUDENTS NAME	NAME OF THE COLLEGE	CATEGORY	STATE
1	U15EC001-ABISHEK	Bharath University	Student	TamilNadu
2	U15EC002-AKIRUL ISLAM	Bharath University	Student	TamilNadu
3	U15EC003-ANAND RAHUL	Bharath University	Student	TamilNadu
4	U15EC004-ANGEL	Bharath University	Student	TamilNadu
5	U15EC005-ANJALIKUMARI	Bharath University	Student	TamilNadu
6	U15EC006-ARAVIND.V	Bharath University	Student	TamilNadu
7	U15EC007-ASHIKQUE ALAM	Bharath University	Student	TamilNadu
8	U15EC008-ASVATHAMAN	Bharath University	Student	TamilNadu
9	U15EC009-BABLOOKUMAR	Bharath University	Student	TamilNadu
10	U15EC010-BALRAM YADAV	Bharath University	Student	TamilNadu
11	U15EC011-CLIFORD	Bharath University	Student	TamilNadu
12	U15EC012-DHAMODHARAN	Bharath University	Student	TamilNadu
13	U15EC013-DEEPAK KUMAR	Bharath University	Student	TamilNadu
14	U15EC014-DEEPAN RAJ	Bharath University	Student	TamilNadu
15	U15EC015-DEEPSHIKA KUMARI	Bharath University	Student	TamilNadu
16	U15EC016-PRİYANKA	Bharath University	Student	TamilNadu
17	U15EC017-ELVIS RYNTONG	Bharath University	Student	TamilNadu
18	KIRAN REDDY	Agni College of Technology	Student	TamilNadu
19	ISHWARYA	SRM College of Engineering and Technology	Student	TamilNadu
20	JAYED AHMED	Dhanalakshmi College of Engineering	Student	TamilNadu
21	KUMARI PRIYANKA	Dhanalakshmi College of Engineering	Student	TamilNadu
22	ANIS MD	Dhanalakshmi College of Engineering	Student	TamilNadu
23	MURALITHARAN	Dhanalakshmi College of Engineering	Student	TamilNadu
24	NABIL NAIDU	KPR Institute of Engineering and Technology	Student	TamilNadu
25	PAVITHRA	Bharath University	Student	TamilNadu
26	NAVEEN	SNS College of Technology	Student	TamilNadu
27	NEERAJ	SNS College of Technology	Student	TamilNadu
28	DEEPSHIKA KUMARI	SNS College of Technology	Student	TamilNadu
29	HARESH	SNS College of Technology	Student	TamilNadu

30	SARATH BABU	Bharath University	Student	TamilNadu
31	TAMIZH THENDRAL	Bharath University	Student	TamilNadu
32	SUGENTHAN	Bharath University	Student	TamilNadu
33	UDHAYAN	SNS College of Technology	Student	TamilNadu
34	LOKESH	Bharath University	Student	TamilNadu
35	JAISHANKAR	SRM College of Engineering and Technology	Student	TamilNadu
36	ABHISHEK	Agni College of Technology	Student	TamilNadu
37	HARESH	Agni College of Technology	Student	TamilNadu
38	SRIJAN KUMAR	SRM College of Engineering and Technology	Student	TamilNadu
39	SADHANA	SRM College of Engineering and Technology	Student	TamilNadu
40	SUMIT	Dhanalakshmi College of Engineering	Student	TamilNadu



Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
WORKSHOP ON ANALOG VLSI DESIGN

FEED BACK FORM		Date: 23/10/2017			
Name	Anis.MD				
Phone number	9224486862				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	


Student Signature



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
WORKSHOP ON ANALOG VLSI DESIGN

FEED BACK FORM		Date: 24/10/2017			
Name	SUMIT				
Phone number	9444872311				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	

Sumit
Student Signature



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

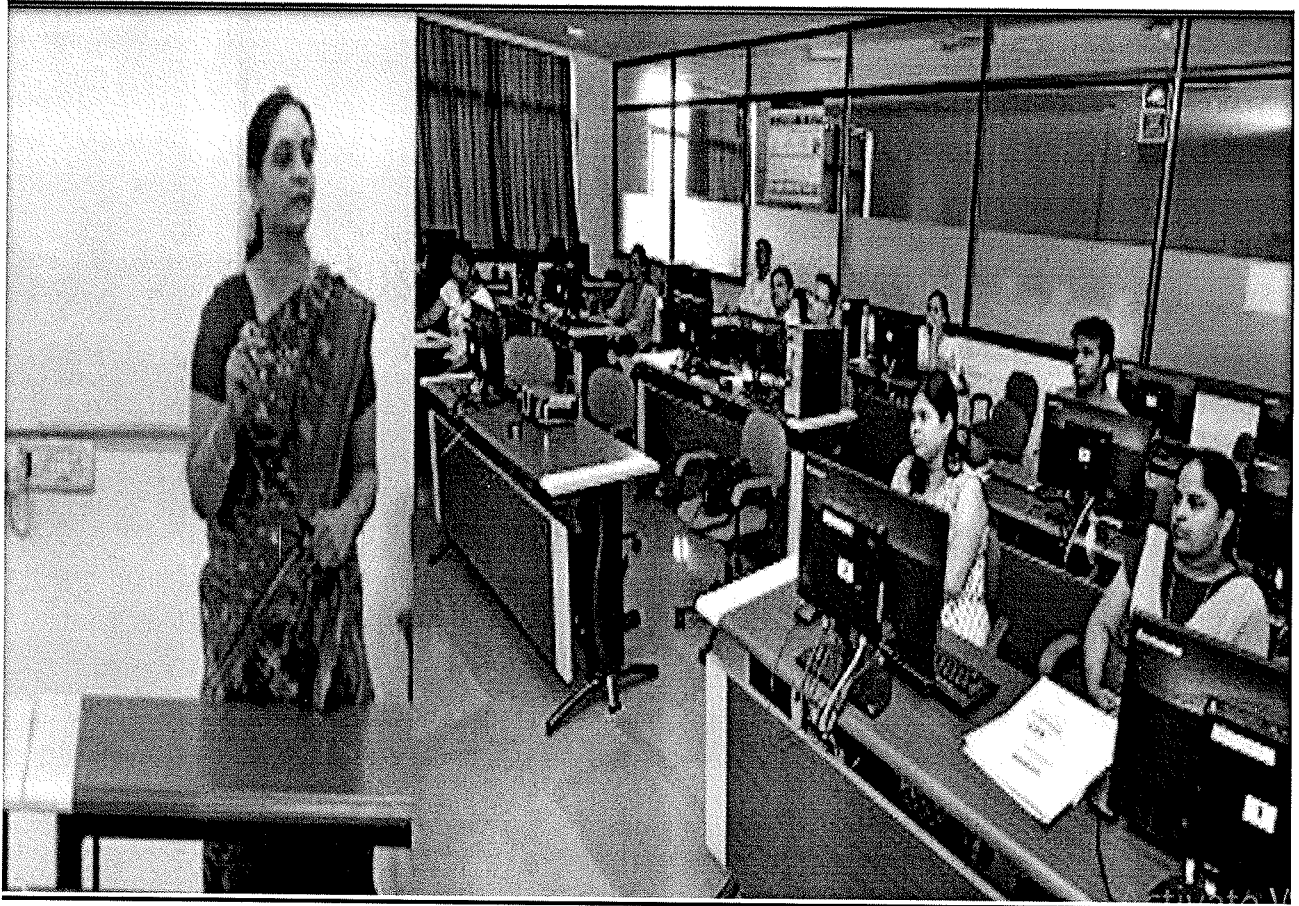
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Sefaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
PHOTOS



**Students are attending a Workshop on “Analog VLSI Design” on
23.10.2017 & 24.10.2017.**



Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selalalur, Chennai, T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
CERTIFICATE



Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)
ACCREDITED WITH 'A' GRADE BY NAAC

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATION OF PARTICIPATION

This is to certify that _____ has attended two days workshop on "Analog VLSI Design" Organized by the Department of Electronics and Communication Engineering, BIHER in association with 'The London Northwest NHS Trust' conducted on 23.10.2017 & 24.10.2017.

COORDINATORS

Ms. G. Jeyalakshmi,
Ms. B. Kalaiselvi

CONVENOR

Dr.M.Sangeetha

DEAN ENGINEERING

Dr.J.Hameed Hussain