



# Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073.

## INTERNAL QUALITY ASSURANCE CELL (IQAC)

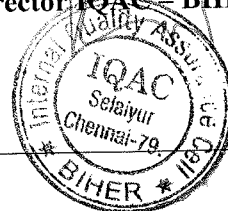
### DOCUMENTS

<b>Date of Submission</b>	18-02-2018
<b>Type of Documents</b>	Workshop
<b>Description</b>	Workshop on Embedded Systems on Chip Lab
<b>Enclosures</b>	a) REQUISITION LETTER b) PERMISSION LETTER c) CIRCULAR d) SCHEDULE e) INVITATION f) BROCHURE g) LIST OF PARTICIPANTS h) FEEDBACK FORM i) PHOTO j) CERTIFICATE
<b>No. of Pages</b>	16
<b>Submitted By</b>	<b>Name</b> : Dr. M.Sangeetha <b>Designation</b> : Professor &HOD/ECE <b>Department</b> : ECE <b>Signature</b> :

### For Office Use Only

<b>Verified By:</b>	K. Saktivel	<b>Sign:</b>		<b>Date:</b>	18/2/2018
<b>Uploaded By:</b>	K. Senthil Kumar	<b>Sign:</b>		<b>Date:</b>	18/2/2018
<b>File Name:</b>	WORK-SEE-ECE-2017-2018-07				

Director IQAC - BIHER





# Bharath

## INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



### BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

#### Requisition Letter

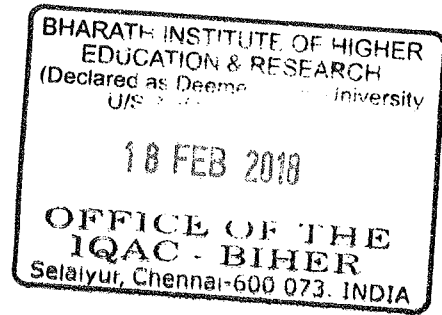
Date: 24.01.18

From

The HOD,  
Department of ECE,  
Bharath Institute of Higher Education and Research,  
Chennai.

To

The Dean Engineering,  
Bharath Institute of Higher Education and Research,  
Chennai.



Respected Sir,

Subject: Request of Permission to conduct "Two days workshop on Embedded Systems on Chip Lab" -Reg.

With reference to above subject, I would like to bring to your kind notice that, our department interested to organize "Two days workshop on Embedded Systems on Chip Lab" in our campus premises on 08.02.2018 & 09.02.2018 between 9.00 AM to 04.00 PM. In this regard, I kindly request you to grant permission for conducting the same.

Thanking you

HOD/ECE

DEAN ENGINEERING



# Bharath

**INSTITUTE OF HIGHER EDUCATION AND RESEARCH**

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**BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY**

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

**PERMISSION LETTER**

**28.01.2018**

From

The HOD,  
Department of ECE,  
School of Electrical Engineering,  
BIHER.

To

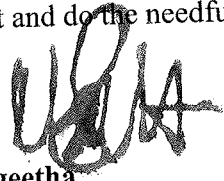
Dr. L .K. Hema  
Prof.& Head ECE  
AarupadaiVeedu Institute of Technology  
Chennai.

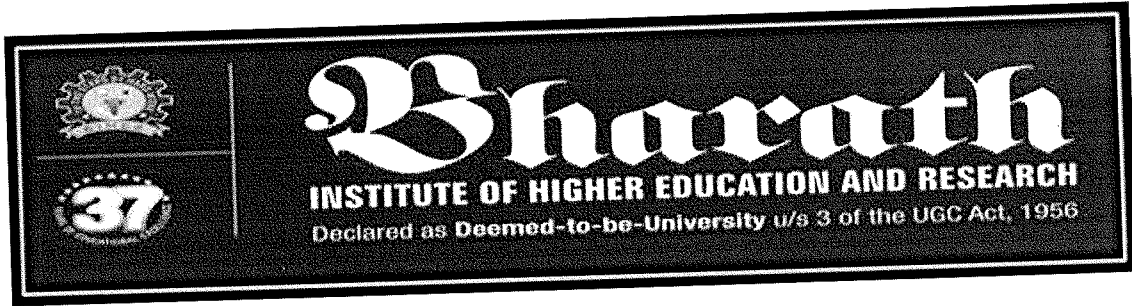
Respected sir,

Sub: - The Department of Electronics and Communication Engineering, School of Electrical Engineering, BIHER, proposed to conduct a workshop in “**Embedded systems on Chip Lab**”- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “**Embedded systems on Chip Lab** “in association with INDO-BRITISH FRCR GROUP ( Bradford Teaching Hospitals, United Kingdom) on 08.02.2018& 09.02.2018.Kindly accept our request and do the needful .

Convener

  
**Dr.M.Sangeetha**  
**HOD/ECE**



**CIRCULAR**

**SCHOOL OF ELECTRICAL ENGINEERING**

**Date: 30.01.2018**

The Workshop on “**Embedded systems on Chip Lab Lab**” is planned by the School of Electrical Engineering on 08.02.2018 & 09.02.2018. This Workshop Programme is offered by Department Of ECE, Bharath Institute of Higher Education and Research, with the duration of two days (08.02.2018 & 09.02.2018). In this regard, Students are instructed to give their willingness and confirm their participation to their respective class in charge/workshop Coordinator before **01.02.2018**

**Dr.B.Karthik**

**Associate Professor/ECE, School of electrical Engineering**

**Workshop Coordinator**

**Bharath Institute of Higher Education and Research**

**Email id: karthikleat@gmail.com**

**Dr.M.Sangeetha  
HOD/ECE**

# **Bharath Institute of Higher Education and Research**

## **Department of ECE**

### **Program Schedule**

**08.02.2018**

<b>Time</b>	<b>Event</b>
<b>9.30 – 9.40 Am</b>	<b>Inaugural Address</b>
<b>9.40 – 9.50 Am</b>	<b>Keynote Address</b>
<b>9.50 - 10.50 Am</b>	<b>Session 1</b>
<b>10.50 - 11.00 Am</b>	<b>Tea Break</b>
<b>11.00 - 12.30 Pm</b>	<b>Session 2</b>
<b>12.30 - 1.30 Pm</b>	<b>Lunch Break</b>
<b>1.30 - 3.45 Pm</b>	<b>Session 3</b>
<b>3.45 - 4.00 Pm</b>	<b>Vote of Thanks</b>



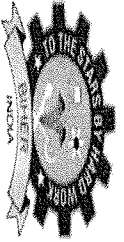
# **Bharath Institute of Higher Education and Research**

## **Department of ECE**

### **Program Schedule**

**09.02.2018**

<b>Time</b>	<b>Event</b>
<b>9.30 – 9.40 Am</b>	<b>Inaugural Address</b>
<b>9.40 – 9.50 Am</b>	<b>Keynote Address</b>
<b>9.50 - 10.50 Am</b>	<b>Lab Session</b>
<b>10.50 - 11.00 Am</b>	<b>Tea Break</b>
<b>11.00 - 12.30 Pm</b>	<b>Lab Session</b>
<b>12.30 - 1.30 Pm</b>	<b>Lunch Break</b>
<b>1.30 - 3.45 Pm</b>	<b>Lab Session</b>
<b>3.45 - 4.00 Pm</b>	<b>Vote of Thanks</b>



**Bharath**



**INSTITUTE OF HIGHER EDUCATION AND RESEARCH**

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NAAC

**INSTITUTE OF SCIENCE AND TECHNOLOGY**

**NO.173, Asharam Road, Selaiyur, Chennai - 600 073.**

## School of Electrical Engineering

In association with

**INDO-BRITISH FRCR GROUP (Bradford Teaching Hospitals, United Kingdom)**

The Management, Chancellor, Vice Chancellor, Deans, HODs, Faculty and Students of School of Electrical Engineering

Cordially Invite you for the two days Workshop on  
"Embedded systems on Chip Lab"

Resource Person  
Keynote Address

: Dr. L. K. Hema, Prof & Head ECE, Aarupadai Veedu Institute of Technology, Chennai.

: Dr.J.Hameed Hussian, Dean Engineering, BIHER.

SPECIAL ADDRESS

: Dr.M.Sangeetha, Professor/HOD, ECE, BIHER

PROGRAM COORDINATOR : Dr.B.Karthik, Associate Professor, ECE, BIHER

VENUE

: ECE SMART ROOM, SA BLOCK, BIHER

DATE

: 08.2.2018 & 09.2.2018

TIME

: 9.00 AM -4.00 PM

Organized by

The Department of ECE,

School of Electrical Engineering,

Bharath Institute of Higher Education and Research

Chennai-6000073

### REGISTRATION FORM

1. Name : \_\_\_\_\_
2. Department : \_\_\_\_\_
4. Designation : \_\_\_\_\_
5. Qualification : \_\_\_\_\_
6. Mobile Number : \_\_\_\_\_
7. Contact Address : \_\_\_\_\_
8. E-Mail ID : \_\_\_\_\_

**REGISTRATION FEE: RS 750**

### PAYMENT DETAILS:

D.D. No: \_\_\_\_\_ Date: \_\_\_\_\_

Drawn on Bank : \_\_\_\_\_

Amount: \_\_\_\_\_

Signature \_\_\_\_\_ Head of Institution

Date: \_\_\_\_\_  
(Photocopies of the form accepted)

### CO-ORDINATOR:

**Dr.B.Karthik**  
Associate Professor, ECE

### VENUE:

Smart Room  
Department of Electronics and  
Communication Engineering, BIHER  
Chennai.

### COURSE CONTENT

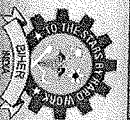
- Introduction to Advanced Embedded Architecture
- Recent developments in Embedded Architecture
- Real Time Operating Systems
- Implementing Real time algorithm
- Multitasking
- ARM processor Interfacing
- Hands on with ARM processor Co-Patron

### RESOURCE PERSONS

1. Dr. L. K. Hema Prof.& Head ECE  
AarupadaiVeedu Institute of Technology,  
Chennai.
2. Mr. Lakshmi Narayana  
Assistant Professor, (SR Grade)  
Peri Institute of Technology, Chennai.
3. Mr Balakrishnan,  
Senior Design Engineer  
Foreview Technologies, Chennai.

### CONVENER

**Dr.M.Sangeetha**  
Prof & Head/ECE



**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Approved & Affiliated to the University under Section 19 of UGC Act 1956)

**BHARATH INSTITUTE OF HIGHER  
EDUCATION AND RESEARCH**

173, Agaram road, Selaiyur, Chennai-600073

**WORKSHOP**

**ON**

**Embedded systems on Chip**

**08<sup>th</sup> and 09<sup>th</sup> Feb 2018**

**BY**

**DEPARTMENT OF ELECTRONICS AND  
COMMUNICATION ENGINEERING**

**SCHOOL OF ELECTRICAL SCIENCES**



[www.bharathuniv.ac.in](http://www.bharathuniv.ac.in)

# Bharath Institute of Higher Education and Research

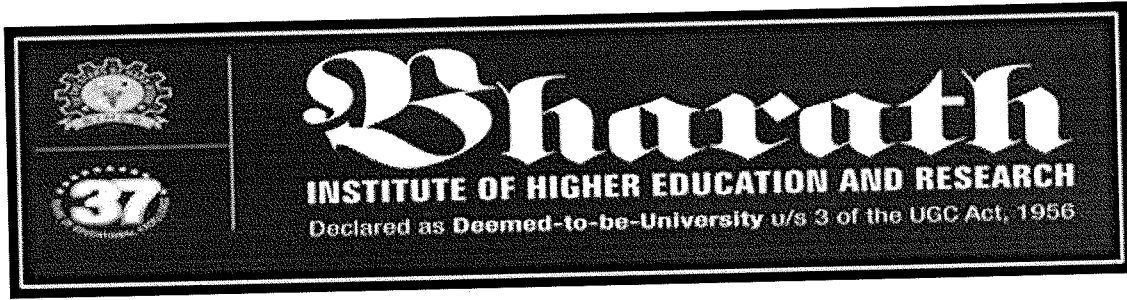
## Department of ECE

### PARTICIPANTS LIST

S.No	Name of the Participant	CATEGORY	INSTITUTE	City	STATE
1.	A.Saranya	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
2.	Sidharthraj.R.S	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
3.	R. Sathya Narayan	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
4.	Ms. S. Philomina	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
5.	K. Bala Phani Kumar	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
6.	Sivashankari N	FACULTY	St.Joseph's Institute of Technology	Chennai	TAMIL NADU
7.	Mohamed Thanfiq A	FACULTY	St.Joseph's Institute of Technology	Chennai	TAMIL NADU
8.	Lakshya Krishna	FACULTY	BIHER	Chennai	TAMIL NADU
9.	Mohamd Kashifudin.B	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
10.	K.Shanmugasri	FACULTY	St.Joseph's Institute of Technology	Chennai	TAMIL NADU
11.	M.Sai Karthik	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
12.	Mrs. Sowmiya Manoj	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
13.	Naveen A Winson	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
14.	K. Divyasree	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
15.	P.Ramana Reddy	FACULTY	BIHER	Chennai	TAMIL NADU

16.	Sasidharan Ravikumar	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
17.	Dr.B.Karthik	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
18.	Ms. K.Suja	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
19.	Mantu Kumar Singh	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
20.	Kalumuri Vijaya Kumar Reddy	FACULTY	Bharath institute of higher education and research, Chennai	Chennai	TAMIL NADU
21.	Shanmugapriyan. K	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
22.	Kanike Saiprakash	FACULTY	Bharath institute of higher education and research, Chennai	Chennai	TAMIL NADU
23.	Kanagadurga. L	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
24.	N.Sankirth Kumar	FACULTY	Bharath institute of higher education and research, Chennai	Chennai	TAMIL NADU
25.	P.Susheel Kumar	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
26.	Mr.R.Mohan Raj	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
27.	P.Nivetha	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
28.	Palaparthi Rambabu	FACULTY	BIHER, Chennai	Chennai	TAMIL NADU
29.	Nandhini.S	FACULTY	R.M.K. College of Engineering And Technology	Chennai	TAMIL NADU
30.	Mr.P.Dharani Sai	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
31.	S.Sabareessh	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
32.	Ms.D.Pradishta	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU
33.	G.Pranay Kumar	FACULTY	Bharath institute of higher education and research, Chennai	Chennai	TAMIL NADU
34.	Monisha.S	FACULTY	R.M.K. College of Engineering And Technology	Chennai	TAMIL NADU
35.	Pradeep Kumar.R.M	FACULTY	St. Joseph's Institute of Technology	Chennai	TAMIL NADU

36.	G.Nithin Reddy	FACULTY	Bharath institute of higher education and research, Chennai	Chennai	TAMIL NADU
37.	R .Sai Varma,	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
38.	N.Manoj Kumar	FACULTY	Bharath institute of higher education and research	Chennai	TAMIL NADU
39.	Mr.Vijayanandh.R	FACULTY	R.M.K. College of Engineering And Technology	Chennai	TAMIL NADU
40.	Mrs.A.Maria Christina Blessy	FACULTY	St. Joseph's Institute of Technology.	Chennai	TAMIL NADU



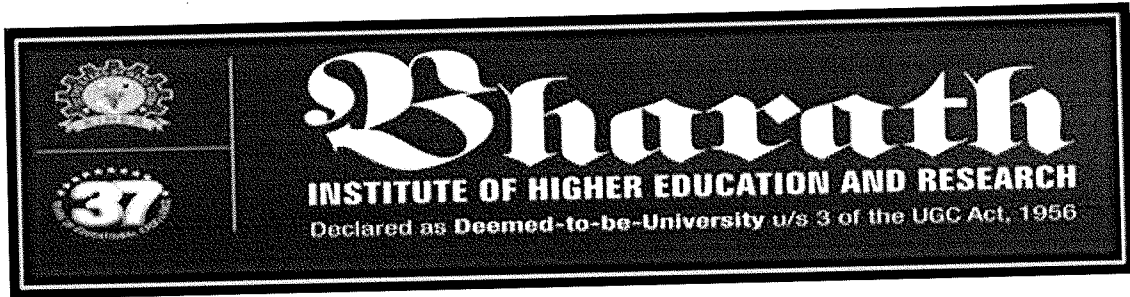
**SCHOOL OF ELECTRICAL ENGINEERING**

**FEEDBACK FORM**

**WORKSHOP ON EMBEDDED SYSTEM ON CHIP LAB**

FEED BACK FORM		Date:08.02.2018			
Name	R. Saivarma				
Phonenumber	9176773434				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
TheSpeaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	

*Saivarma*  
Student Signature



**SCHOOL OF ELECTRICAL ENGINEERING**

**FEEDBACK FORM**

**WORKSHOP ON EMBEDDED SYSTEM ON CHIP LAB**

FEED BACK FORM		Date:09.02.2018			
Name	N. Sabareesh				
Phonenumber	9224433122				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	

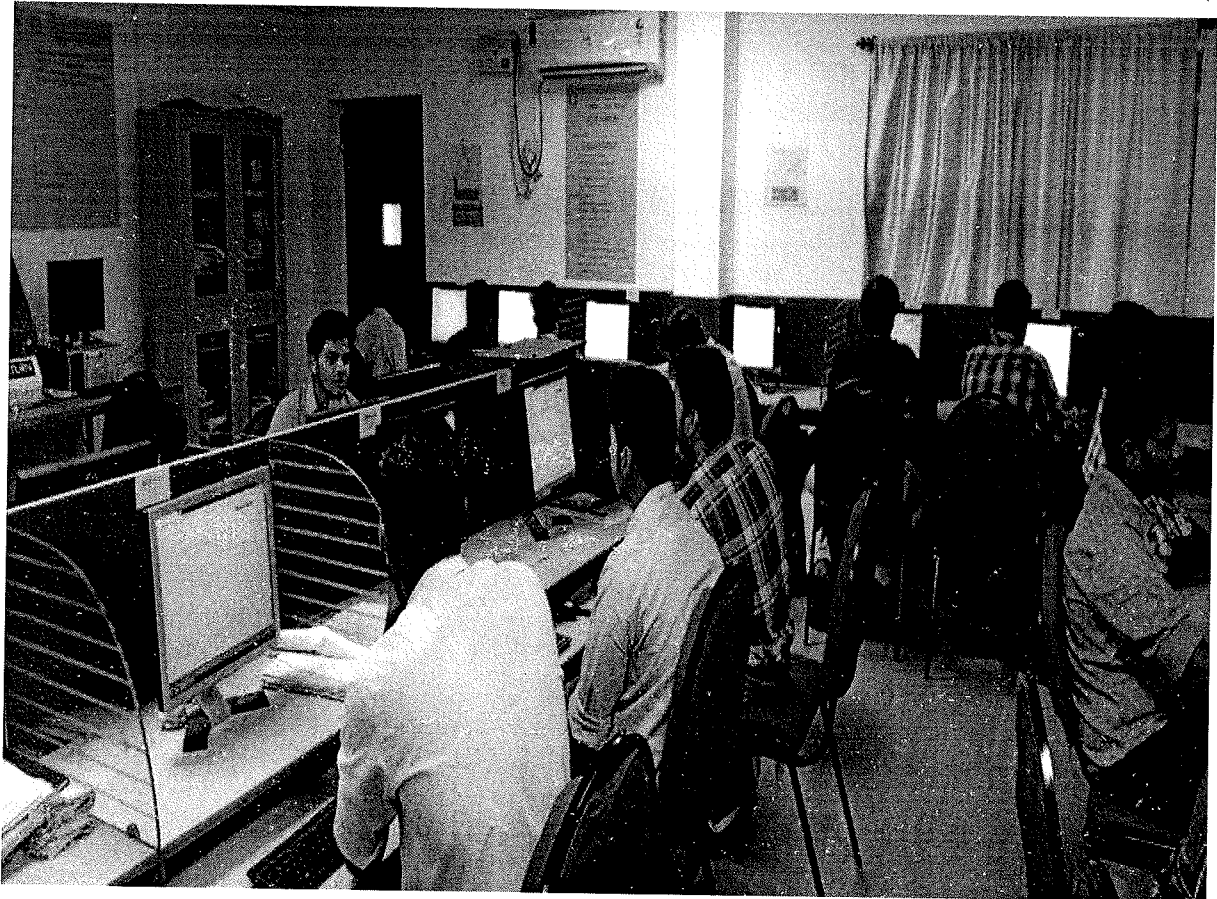
  
**Student Signature**

**Bharath Institute of Higher Education and Research**

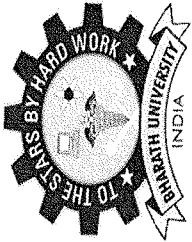
**Department of ECE**

**WORKSHOP ON EMBEDDED SYSTEM ON CHIP LAB**

**PHOTOS**







**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
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ACCREDITED WITH 'A' GRADE BY NAAC

## SCHOOL OF ELECTRICAL ENGINEERING

### CERTIFICATION OF PARTICIPATION

*This is to certify that Mr/Mrs. K. Bala Phani Kumar has attended two days Workshop on Embedded systems on Chip Lab Organized by the Department of Electronics and Communication Engineering, BHER conducted from 8.2.2018 to 9.2.2018*

*Dr.B.Karthick*

**COORDINATOR**  
**Dr.B.Karthick**

*Dr.M.Sangeetha*

**CONVENOR**  
**Dr.M.Sangeetha**