




Bharath Institute of Higher Education and Research



[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073.

INTERNAL QUALITY ASSURANCE CELL (IQAC)

| | |
|--------------------|--|
| Date of Submission | 17-07-2019 |
| Type of Documents | SEMINAR |
| Description | SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES |
| Enclosures | a) REQUISITION LETTER b) PERMISSION LETTER c) CIRCULAR d) SCHEDULE e) INVITATION f) BROCHURE g) LIST OF PARTICIPANTS h) FEEDBACK FORM FROM PARTICIPANTS i) PHOTO & CERTIFICATE |
| No. of Pages | 15 |
| Submitted By | Name : Dr. M.Sangeetha |
| | Designation : Professor &HOD/ECE |
| | Department : ECE |
| | Signature :  |

DOCUMENTS SUBMISSION FOR

For Office Use Only

| | | | |
|--------------|--------------------------------------|--|-----------------|
| Verified By: | K - Sakthivel | Sign:  | Date: 17/7/2019 |
| Uploaded By: | K - Senthil Kumar | Sign:  | Date: 17/7/2019 |
| File Name: | STEMI - BEE - ECE - 2019 - 2020 - 07 | | |

Director IQAC – BIHER





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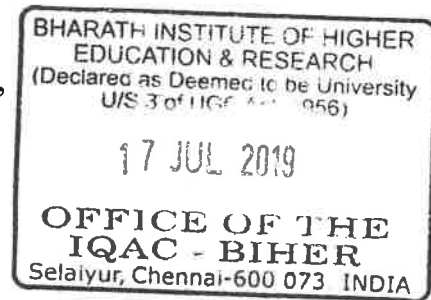


BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Requisition Letter

Date: 02.07.2019


From
The HOD,
ECE Department,
Bharath Institute of Higher Education and Research,
Selaiyur, Chennai.



To
The Dean Engineering,
Bharath Institute of Higher Education and Research,
Selaiyur, Chennai.

Respected Sir,

SUB : Requisition for conducting seminar on online chip design analysis -Reg
Department of Electronics and Communication Engineering of School of Electrical
Engineering has planned to conduct seminar on System on chip design challenges
on 11-07-2019 between 9.30 AM to 4.30 PM. In this regard we kindly request you
to grant permission for the same.


HOD/ECE

Thanking you




Dean Engineering
DEAN (Engineering)
Bharath Institute of Science & Technology
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
(Declared as Deemed to be University U/S 3 of UGC Act. 1956)
Selaiyur, Chennai-600 073.



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

30.10.2017

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER.

To
Jitendra Prasad,
Area business Head and Network,
Bright Star Telecom,
Chennai.

Respected sir,

Sub:- The Department of Electronics and Communication Engineering, School of Electrical Engineering, BIHER, proposed to conduct Seminar on “System on chip design challenges” - Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “System on chip design challenges”, on 11.07.2019. Kindly accept our request and do the needful .


Convener
Dr.M.Sangeetha, HOD/ECE



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selsiyur, Chennai , T.N - 600 073.

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 05.07.2019

The School of Electrical Engineering, Department of ECE, BIHER is glad to conduct one day seminar on “**SYSTEM ON CHIP DESIGN CHALLENGES**” dated on 11.07.2019 in association with Bright star Telecom, Chennai, between 9.30AM to 4.30PM. Those who are interested to participate do register your name with the program coordinator mentioned below.

Resource Person:

**Jitendra Prasad,
Area business Head and Network,
Bright Star Telecom,
Chennai.**

Convener:

**Dr.M.Sangeetha,
HOD/ECE**

Program Coordinator:

**Ms.Sowmiya Manoj, Mr.T.Vijayan
Assistant Professor**

**Email:sowmiyamanoj.ece@bharathuniv.ac.in
Contact no:7358747803**



Convener

Dr.M.Sangeetha, HOD/ECE

To,
Copy to ECE Department,
Copy to EEE Department,
Department Notice Board



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

Department of ECE

Program Schedule

11.07.2019

| Time | Event |
|------------------|-------------------|
| 9.30 – 9.40 Am | Inaugural Address |
| 9.40 – 9.50 Am | Keynote Address |
| 9.50 - 10.50 Am | Session 1 |
| 10.50 - 11.00 Am | Tea Break |
| 11.00 - 12.30 Pm | Session 2 |
| 12.30 - 1.30 Pm | Lunch Break |
| 1.30 - 3.45 Pm | Session 3 |
| 3.45 - 4.00 Pm | Vote of Thanks |



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING
WORKSHOP ON SYSTEM ON CHIP DESIGN CHALLENGES

Invitation

School of Electrical Engineering
Department of ECE
BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH
in association with
Bright star Telecom ,Chennai
SEMINAR ON
SYSTEM ON CHIP DESIGN CHALLENGES

Resource Person:Mr Jitendra Prasad,
Keynote Address: Dr. J. Hameed Hussain, Dean Engineering, BIHER, Chennai
Special Address: Dr. M. Sangeetha, Professor & Head/ ECE, BIHER
Program Coordinator: Mrs. Sowmiya manoj & Mr. T. Vijayan

Venue: Smart Room, ECE, BIHER
Date :11.07.2019
Time : 11.00 am to 3.00 pm



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING

ONEDAY SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES

BROCHURE

| | | |
|---|---|---|
| <p>Registration form: Name : Designation: Name of the Title : Area of Research : Organization : Address for Communication :</p> | <p>Advisory Committee: Dr.J Sundoop Anand Chancellor/BIHER Dr.S Saroetha Anand Managing Director/BIHER Dr.V.Kanagasabai Vice chancellor/BIHER</p> | <div style="text-align: center;">   <p>BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH 173, Agharam road, Selaiyur, Chennai-600 073</p> <p>National Seminar ON "System On Chip Design Challenges" 11th August 2019 Organized by</p> <p>DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING In association with Bright Star Telecom</p>  <p>www.bharathuniv.ac.in</p> </div> |
| <p>Contact No. Email : Category : PG students/Research Scholar/Faculty/Industrial person</p> | <p>Co - Patron Dr.K.P.Thayyaman Pro vice chancellor/BIHER Dr.J.Hameed Hussain Dean Engineering /BIHER Dr.M.Sundararajan Dean School of Electrical science/BIHER</p> | |
| <p>Payment Details : D.D No/ Date : Drawn on Bank : Amount :</p> | <p>Convener Dr.M.Saroetha Professor & HOD/ECE</p> <p>Program Committee: MS.Sowmya manoj Assistant Professor/ECE</p> | |
| <p>Signature Head of Institution</p> | <p>Technical Committee: Dr.S.Philamina Assistant Professor/ECE G.Jeyalakshmi Assistant Professor/ECE</p> | |
| <p>Date : (Photo copies of the form accepted)</p> | <p>Organizing Secretary: S. Kishore Karthik GT Student of ECE Kunasan Hemamath Student of ECE</p> | |

| | | |
|--|---|---|
| <p>About the University: The first self-financing Engineering College in the name of Bharath Institute of Science and Technology (BIST) was started in Tamil Nadu in 1984 by Sri Lalitha Ammal Educational Trust, established by the academic visionary Dr.S.Jagathrakshakan.</p> <p>Bharath University and is known for providing high-quality education to around 10000 students from across the world in multi-stream. The institute facilitates the guidance of 1300 teaching faculty members to provide guidance to do Undergraduate, Postgraduate programs and to facilitate Ph.D. research.</p> <p>About the Department: Electronics and Communication Engineering (ECE) is a swiftly advancing field, with new ideas emerging every other seconds. Graduate engineers in this discipline will be equipped to design and fabricate, install, operate and maintain complex electronic circuits, equipment's and systems.</p> <p>The course also covers designing security in communications, besides all the software and hardware required in the communication domain.</p> | <p>About the Seminar: A <i>one-day</i> seminar on a <i>chip</i> (SoC) consists of several different processes which are together with success and I/O peripherals. This course covers SoC design and modelling techniques with emphasis on architectural exploration, hardware-driven design and the concurrent development of hardware and embedded software. This is the "front end" of the design automation tool chain. (Back end contains, such as design of structural pads, layout routing and fabrication of silicon chips is mentioned but not executable.) A percentage of each lecture is used to develop a running example. Over the course of the lecture, the example machine runs a System On Chip demonstration with CPU and bus models, device models and device drivers. All code and tools are available online so the examples can be reproduced and executed immediately. The exam language used are Verilog and C++ using the System C library.</p> <p>Seminar Structure: Verilog RTL design with examples. Event driven simulation with and without delta cycles, back gate synthesis algorithm and design examples. Structural hazards (memories and multipliers) Pipelining and handshake synthesis. [3 lectures] SystemC overview. The major components of the SystemC</p> | <p>C++ class library for hardware modelling are covered with code fragments and demonstrations.</p> <p>Queueing/contention delay modelling. Power, energy and layout high level modelling. [2 lectures] Basic bus structures. Bus structure. I/O device structure. Interrupts, DMA and device drivers. Examples. Basic bus bridging</p> <p>The Research scholars and students, Faculties can confirm their registration by sending the DD. The DD should take in favour of "BIHER Chennai" before 02.07.2019, backside of DD carry full name, contact number and address of the author.</p> <p>Selected candidates list announced through email. Accommodation shall be arranged on request at a reasonable cost.</p> <p>Dates to be Remember :</p> <p>Last date for Registration: 05/07/2019 Short listed candidates list announcement: 08/07/2019 Registration fee details: Students : Rs.250/ Research scholars & Staff members : Rs.500/ Industry Person : Rs.750/</p> |
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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING

ONE DAY SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES

LIST OF PARTICIPANTS

TOTAL NUMBER OF PARTICIPANTS:50

| S.NO | NAME OF THE PARTICIPANT | CATEGORY | INSTITUTE | CITY | STATE |
|------|-------------------------|----------|--|---------|-----------|
| 1 | S.SARAVANA | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 2 | SOWMIYA MANOJ | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 3 | G.KANAGAVALLI | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 4 | M.JASMIN | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 5 | B.HEMALATHA | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 6 | K.SUBBULAKSHMI | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | CHENNAI | TAMILNADU |
| 7 | BORA GANESH | STUDENT | St. Joseph's Institute of Technology | CHENNAI | TAMILNADU |
| 8 | MUNTA PRAVEEN KUMAR | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |

| | | | | | |
|----|------------------------|---------|--|---------|-----------|
| 9 | MATHIUKHONANG | STUDENT | St.Joseph's Institute Of Technology, Chennai | CHENNAI | TAMILNADU |
| 10 | SANGAVI B | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 11 | A.Nivedha | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 12 | M.Kavya | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 13 | AJITH A K | STUDENT | St.Joseph's Institute of Technology | CHENNAI | TAMILNADU |
| 14 | SRIRAM GANES S | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 15 | GALLA BALAJI | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 16 | P NITHIN KRISHNA | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 17 | THIRUKUMARAN R | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 18 | BARATH D | STUDENT | St.Joseph's Institute of Technology | CHENNAI | TAMILNADU |
| 19 | ALICATTA AVINASH | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 20 | C BHARATHRAJ | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 21 | ALLA RAJA SEKHAR REDDY | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 22 | SHALINI.K | STUDENT | St.Joseph's Institute Of Technology,Chennai | CHENNAI | TAMILNADU |

| | | | | | |
|----|-----------------------------|---------|--|---------|-----------|
| 23 | HAMEED ANWAR.D | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |
| 24 | EDWARD.J | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 25 | JAYASEELAN.H | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 26 | ARUNA.L | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |
| 27 | MANIKAM.B | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 28 | GALLA BALAJI | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 29 | SHREYA SHUBHANGI | STUDENT | St. Joseph's Institute Of Technology, Chennai | CHENNAI | TAMILNADU |
| 30 | THIRUKUMARAN R | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 31 | NAGA MEGASIVA VARDHAN REDDY | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 32 | ALICATTA AVINASH | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 33 | ABIRAMI B | STUDENT | St. Joseph's Institute Of Technology, Chennai | CHENNAI | TAMILNADU |
| 34 | SRIRAM GANES S | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |
| 35 | THARUN.S | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 36 | SANKAR.A | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 37 | MALAVIKA.S | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 38 | KATHIR.M | STUDENT | St. Joseph's Institute Of Technology, Chennai | CHENNAI | TAMILNADU |

| | | | | | |
|----|---------------------|---------|--|---------|-----------|
| 39 | KARTHI.L | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |
| 40 | SHARAN.C | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 41 | MANSOOR.B | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 42 | MALATHY.S | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 43 | MUNGARA SRIHARIBABU | STUDENT | St.Joseph's Institute Of Technology,Chennai | CHENNAI | TAMILNADU |
| 44 | BARATH D | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |
| 45 | K.KARTHIK | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 46 | NIKESH E | STUDENT | Velammal Engineering College | CHENNAI | TAMILNADU |
| 47 | AKSHAY B | STUDENT | Bharath institute of higher education and research | CHENNAI | TAMILNADU |
| 48 | SRIRAM GANES S | STUDENT | Agni College of Technology | CHENNAI | TAMILNADU |
| 49 | THIRUKUMARAN R | STUDENT | St.Joseph's Institute Of Technology,Chennai | CHENNAI | TAMILNADU |
| 50 | PRAVEEN KUMAR S | STUDENT | St. Joseph's Institute of Technology, Chennai. | CHENNAI | TAMILNADU |



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selalpur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING
SEMINAR ON SYSTEM ONCHIP DESIGN CHALLENGES

FEED BACK FORM

Date: 11-07-2019

| | | | | | |
|------------------------------------|----------------------|------|------|-----------|-----------|
| Name | Edward J | | | | |
| Register number | U17EC033 | | | | |
| Phone number | 9843147891 | | | | |
| Email address | edward.j11@gmail.com | | | | |
| | Poor | Fair | Good | Very Good | Excellent |
| Overall Program | | | ✓ | | |
| The Speaker | | | ✓ | | |
| Audio, Visual Aids Technology used | | | | ✓ | |
| Presentation hand outs | | | | ✓ | |


Student Signature



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SCHOOL OF ELECTRICAL ENGINEERING

SEMINAR ON SYSTEM ONCHIP DESIGN CHALLENGES

| FEED BACK FORM | | Date: 11-07-2019 | | | |
|------------------------------------|-------------------|------------------|------|-----------|-----------|
| Name | Aruna L | | | | |
| Register number | VITEC067 | | | | |
| Phone number | 9843112700 | | | | |
| Email address | aruna.l@gmail.com | | | | |
| | Poor | Fair | Good | Very Good | Excellent |
| Overall Program | | | ✓ | | |
| The Speaker | | | | ✓ | |
| Audio, Visual Aids Technology used | | | | ✓ | |
| Presentation hand outs | | | | ✓ | |


Student Signature



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No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING

ONE DAY SEMINAR ON SYSTEM ON CHIP DESIGN CHALLENGES

CERTIFICATE



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ACCREDITED WITH 'A' GRADE BY NAAC

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATION OF PARTICIPATION

This is to certify that Mr. PRABHAGAR R has attended one day seminar on "System On chip Design Challenges" Organized by the Department of Electronics and Communication Engineering, BIHER conducted on 11.07.2019

Ms. Sowmiya
COORDINATOR

MS.SOWMIYA MANOJ

Dr.M.Sangeetha

CONVENOR

Dr.M.Sangeetha

DEAN ENGINEERING

Dr.J.Hameed Hussain