

Bharath Institute of Higher Education and Research

[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073.

INTERNAL QUALITY ASSURANCE CELL (IQAC)

DOCUMENTS SUBMISSION FORM

| | |
|---------------------------|--|
| Date of Submission | 09-09-2019 |
| Type of Documents | Workshop |
| Description | Workshop on Current Trends in Architecture of Processor |
| Enclosures | a) REQUISITION LETTER b) PERMISSION LETTER c) CIRCULAR d) SCHEDULE e) INVITATION f) BROCHURE g) LIST OF PARTICIPANTS h) FEEDBACK FORM i) PHOTO j) CERTIFICATE |
| No. of Pages | 15 |
| Submitted By | Name : Dr. M.Sangeetha Designation : Professor &HOD/ECE Department : ECE Signature : |

For Office Use Only

| | | | | | |
|---------------------|-------------------------------------|--------------|--|--------------|----------|
| Verified By: | K. Sakthivel | Sign: | | Date: | 9/9/2019 |
| Uploaded By: | K. Senthil Kumar | Sign: | | Date: | 9/9/2019 |
| File Name: | SEE - ECE - WORK - 2019 - 2020 - 05 | | | | |

Director IQAC – BIHER





Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Requisition Letter

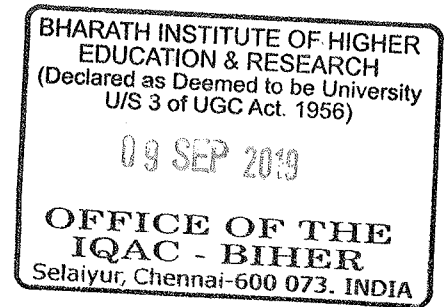
Date:19.08.2019

From

The HOD,
ECE Department,
Bharath Institute of Higher Education and Research,
Selaiyur, Chennai.

To

The Dean Engineering,
Bharath Institute of Higher Education and Research,
Selaiyur , Chennai.



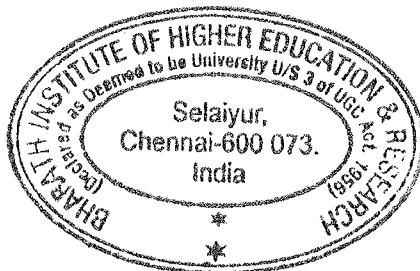
Respected Sir,

SUB : Requisition for conducting Workshop on Current Trends in Architecture of Processors Regd

Department of Electronics and Communication Engineering of School of Electrical Engineering has planned to conduct Workshop on Current Trends in Architecture of Processor on 03-09-2019 and 04-09-2019 between 9.30 AM to 4.30 PM. In this regard we kindly request you to grant permission for the same.

Thanking you

HOD/ECE



Dean Engineering

DEAN (Engineering)

Bharath Institute of Science & Technology
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
(Declared as Deemed to be University U/S 3 of UGC Act. 1956)
Selaiyur, Chennai-600 073.



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

PERMISSION LETTER

Date: 21.08.2019

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER.

To
Dr. A. Ramesh
Prof. & Head CSE
Agni College of Engineering
Chennai .

Respected Madam,

Sub:-The Department of Electronics and Communication Engineering, School of Electrical Engineering, BIHER, proposed to conduct workshop on “Current Trends in Architecture of Processors” - Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “Current Trends in Architecture of Processors” on **03.09.2019** and **04.09.2019**. Kindly accept our request and do the needful.

Convener


Dr.M.Sangeetha, HOD/ECE



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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 28.08.2019

The School of Electrical Engineering, Department of ECE, BIHER is glad to conduct a workshop on “**Current Trends in Architecture of Processor**” dated on **03.09.2019** and **04.09.2019** in association with NavaJyothi Technical Training center & Educational Academy, Chennai, between **9.30AM to 4.30PM**. Those who are interested to participate do register your name with the program coordinator mentioned below.

Resource Person:

Dr. A. Ramesh
Prof. & Head CSE
Agni College of Engineering
Chennai .

Program Coordinators:

Ms. R. Hema
Assistant Professor, ECE
Dr. B. Karthik
Associate Professor, ECE

Convener

Dr.M.Sangeetha, HOD/ECE

To,
Copy to ECE Department,
Copy to EEE Department,
Department Notice Board



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

SCHEDULE

| PROGRAMSCHEDULEDAY(03.09.2019) | |
|---------------------------------------|---|
| 09.30-10.00 AM | InauguralAddress |
| 09.30AM | WelcomeAddressDr.M.Sangeetha |
| 09.35AM | About the WorkshopMs.R.Hema |
| 09.40AM | IntroductionoftheChiefGuestDr. J.Hameed Hussain |
| 09.45AM | InauguraladdressbyChiefGuest Dr. A. Ramesh Prof. & Head CSE Agni College of Engineering Chennai . |
| 10.00 AMOnwards | Invited Talk Dr. A. Ramesh Prof. & Head CSE Agni College of Engineering Chennai . |
| 1.30-2.30PM | Lunchbreak |
| 2.30-04.30pm | WorkshopsessiononCurrent Trends in Architecture of Processor. |

SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

SCHEDULE

| PROGRAMSCHEDULEDay2(04.09.2019) | |
|--|--|
| 9.30-12.30PM | Hands on Training on Keil Software |
| 12.30-1.30PM | Lunchbreak |
| 1.30-3.30PM | Hands on Training on Cadence Software |
| <u>Valedictoryprogram</u> | |
| 03.30-04.00PM | ValedictoryFunction |
| 04.00-04.30PM | VoteofThanks(Coordinator) |



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SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

Invitation



School of Electrical Engineering

In association

With

Navajyothi Technical Training center & Educational Academy, Chennai,

The Management, Chancellor, Vice Chancellor, Deans, HODs, Faculty and Students of School of Electrical Engineering

Cordially invites you for the two days Workshop on

"Two days workshop on "Current Trends in Architecture of Processor"

- Resource Person : Dr. A. Ramesh
Prof. & Head CSE
Agni College of Engineering
Chennai .
- Keynote Address : Dr J.Hameed Hussain
Dean Engineering, BIHER, Chennai
- Special Address : Dr M.Sangeetha
Professor & Head, Department of ECE, Chennai
- Program Coordinator: Ms. R. Hema, Assistant Professor, ECE, BIHER, Chennai
Dr. B. Karthik, Associate Professor, ECE, BIHER, Chennai

Venue:

Smart Room, ECE, BIHER

Date & Time:

03rd & 04th SEPTEMBER 2019; 09.30 AM-04.00 PM



SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

BROCHURE

REGISTRATION FORM

1. Name
2. Department
4. Designation
5. Qualification
6. Mobile Number
7. Contact Address:
8. E-Mail ID

REGISTRATION FEE: RS 750

PAYMENT DETAILS:

D.D.No: Date:

Drawn on Bank:

Amount:

Signature Head of Institution

Date:
(Photocopies of the form accepted)

CO-ORDINATORS:

Ms. R. Hema
Assistant Professor, ECE
Dr. B. Karthik
Associate Professor, ECE

VENUE:

Smart Room
Department of Electronics and
Communication Engineering, BIHER,
Chennai.

COURSE CONTENT

- Introduction to Signal Processing
- Hands on Training on Basic Signal Processing
- Hands on Training on Multirate Processing

RESOURCE PERSON

Dr. A. Ramesh
Prof. & Head CSE
Agni College of Engineering
Chennai.

CONVENER

Dr. M. Sangeetha
Prof. & Head/ECE



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**BHARATH INSTITUTE OF HIGHER
EDUCATION AND RESEARCH**
173, Agharam Road, Selaiyur, Chennai - 600 073

**WORKSHOP
ON
Current Trends in Architecture of
Processor**

17th September 2015, 9:00 AM - 12:00 PM

**ORGANIZED
BY**
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
SCHOOL OF ELECTRICAL SCIENCES



www.bharathuniv.ac.in

SCHOOL OF ELECTRICAL ENGINEERING
WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

LIST OF PARTICIPANTS

TOTAL NUMBER OF PARTICIPANTS:52

| S.NO | NAME OF THE PARTICIPANT | CATEGORY | INSTITUTE | STATE |
|------|---|----------|--|--------------|
| 1 | R.HEMA | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 2 | B.KARTHICK | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 3 | G.KANAGAVALLI | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 4 | M.JASMIN | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 5 | B.HEMALATHA | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 6 | K.SUBBULAKSHMI | FACULTY | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 7 | AJAY K (U18EC001) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 8 | SEDHURAMAN R (U18EC002) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 9 | VERAMATI SATYANARAYANA (U18EC003) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 10 | NAIDU SIVA KUMAR REDDY (U18EC004) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 11 | KARTHICK M V (U18EC005) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 12 | VARAMATI MOHAN KRISHNA (U18EC006) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 13 | CHUNDRU AVINASH (U18EC007) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 14 | GANGAVARAM DIVYASAI (U18EC008) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 15 | KILARI MURALI (U18EC009) | STUDENTS | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 16 | POOJARI RAKESH GOUD (U18EC009) | STUDENTS | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |

| | | | | |
|----|--|---------|--|--------------|
| 17 | PATNAM JASWANT (U18EC010) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 18 | DHARSHAN D (U18EC011) | STUDENT | BIHERBHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 19 | SURAJ VINAY Y (U18EC012) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 20 | YASHVANTH P (U18EC014) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 21 | S K NIZANTH (U18EC015) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 22 | CHINNAMANAYAN APALLI HARISH KUMAR (U18EC016) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 23 | MOWMITHA S (U18EC017) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 24 | VEDULA GOPINADH (U18EC018) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | BIHAR |
| 25 | LENAKU RUKESWAR REDDY (U18EC019) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 26 | KAVULURI PRAVEEN KUMAR REDDY (U18EC020) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 27 | BANISETTI VINAY KUMAR (U18EC021) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH Y | TELEGANA |
| 28 | HANUMANTHU ARAVINDH (U18EC022) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 29 | NEELAM SAICHARAN (U18EC023) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 30 | BOGGULA RAMA KRISHNA REDDY (U18EC024) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 31 | PUTCHAKAYALA PRASAD (U18EC025) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | BIHAR |
| 32 | MONIKA D (U18EC026) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TAMILNADU |
| 33 | VOLETI UDEEP (U18EC027) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 34 | MD ZOHAIB (U18EC028) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |

| | | | | |
|----|---------------------------------|---------|---|--------------|
| 35 | VIBIN SRI BALAJI P(U18EC029) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | ANDRAPRADESH |
| 36 | AJAY K (U18EC030) | STUDENT | BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH | TELEGANA |
| 37 | MADDIBOINA GIRI BABU | STUDENT | AGNI COLLEGE OF TECHNOLOGY | ANDRAPRADESH |
| 38 | MASANAM TARUN | STUDENT | SRM COLLEGE OF ENGINEERING AND TECHNOLOGY | ANDRAPRADESH |
| 39 | N E ANIL KUMAR | STUDENT | DHANALAKSHMI COLLEGE OF ENGINEERING | ANDRAPRADESH |
| 40 | KALLURI RAMA KSRISHNA | STUDENT | SRM COLLEGE OF ENGINEERING AND TECHNOLOGY | TAMILNADU |
| 41 | AKASH KUMAR C | STUDENT | AGNI COLLEGE OF TECHNOLOGY | TELEGANA |
| 42 | BHASHAKARLA GOPIKRISHNA | STUDENT | AGNI COLLEGE OF TECHNOLOGY | TELEGANA |
| 43 | MADDIBOINA GIRI BABU | STUDENT | SRM COLLEGE OF ENGINEERING AND TECHNOLOGY | TELEGANA |
| 44 | MASANAM TARUN | STUDENT | SRM COLLEGE OF ENGINEERING AND TECHNOLOGY | ANDRAPRADESH |
| 45 | N E ANIL KUMAR | STUDENT | DHANALAKSHMI COLLEGE OF ENGINEERING | TELEGANA |
| 46 | KALLURI RAMA KSRISHNA | STUDENT | AGNI COLLEGE OF TECHNOLOGY | TELEGANA |
| 47 | YALAMANDALA SANDHYA | STUDENT | KPR INSTITUTE OF ENGINEERING AND TECHNOLOGY | TELEGANA |
| 48 | RACHARLA VENKATNARAYAN | STUDENT | DHANALAKSHMI COLLEGE OF ENGINEERING | ANDRAPRADESH |
| 49 | A MAHESH | STUDENT | DHANALAKSHMI COLLEGE OF ENGINEERING | ANDRAPRADESH |
| 50 | MARAMREDDY MURALI MOHAN | STUDENT | AGNI COLLEGE OF TECHNOLOGY | ANDRAPRADESH |
| 51 | JIJARAPU SHIVA | STUDENT | DHANALAKSHMI COLLEGE OF ENGINEERING | TELEGANA |
| 52 | DINESH D | STUDENT | KPR INSTITUTE OF ENGINEERING AND TECHNOLOGY | TELEGANA |



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SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

FEED BACK FORM

Date: 3/9/2019

| Name | AJAY.K | | | | |
|------------------------------------|----------------|------|------|-----------|-----------|
| Register number | - | | | | |
| Phonenumber | 9443322272 | | | | |
| Email address | ajay@gmail.com | | | | |
| | Poor | Fair | Good | Very Good | Excellent |
| Overall Program | | | | | |
| The Speaker | | | | ✓ | |
| Audio, Visual Aids Technology used | | | | ✓ | |
| Presentation hand outs | | | | ✓ | ✓ |


Student Signature



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SCHOOL OF ELECTRICAL ENGINEERING

WORKSHOP ON CURRENT TRENDS IN ARCHITECTURE OF PROCESSOR

FEED BACK FORM

Date: 4/9/2019

| | | | | | |
|------------------------------------|------------------|------|------|-----------|-----------|
| Name | MONIKA . D | | | | |
| Register number | | | | | |
| Phonenumber | 9444333222 | | | | |
| Email address | monika@gmail.com | | | | |
| | Poor | Fair | Good | Very Good | Excellent |
| Overall Program | | | | ✓ | |
| The Speaker | | | | ✓ | |
| Audio, Visual Aids Technology used | | | | | |
| Presentation hand outs | | | | ✓ | ✓ |

Student Signature *Monika D.*



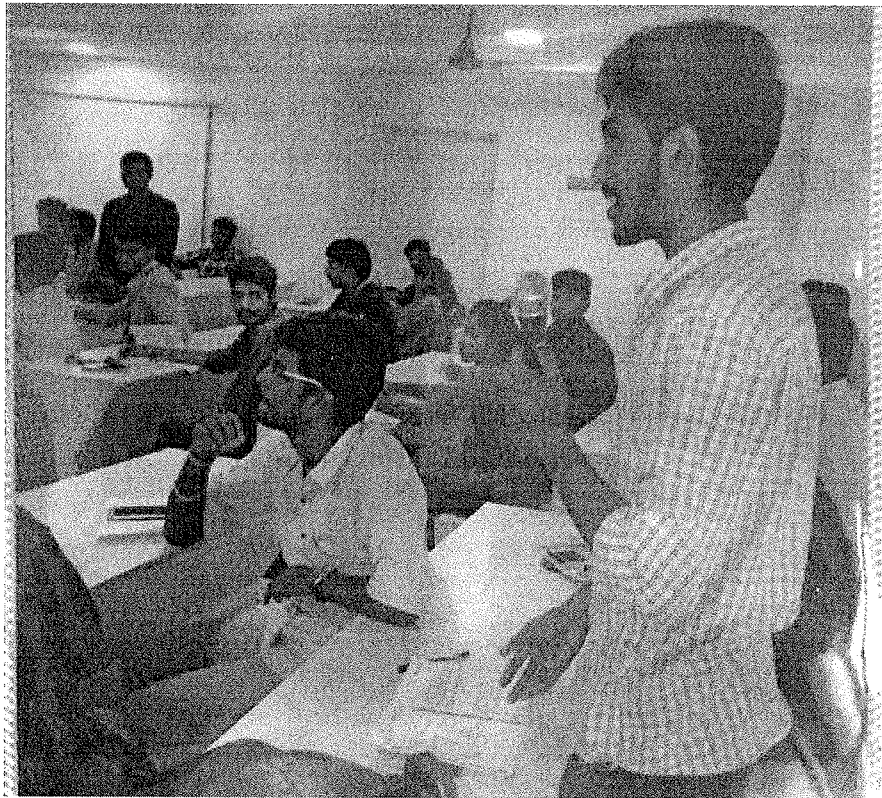
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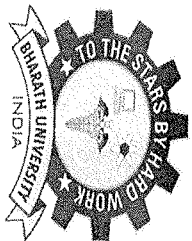


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SCHOOL OF ELECTRICAL ENGINEERING

**Workshop on Current Trends in Architecture of Processordated on 03.09.2019 and
04.09.2019 conducted by School of Electrical Engineering**





BHABHA UNIVERSITY

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ACCREDITED WITH 'A' GRADE BY NAAC

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATION OF PARTICIPATION

This is to certify that GANGAVARAM DIVYASAI has attended two days workshop on "Current Trends in Architecture of Processors" Organized by the Department of Electronics and Communication Engineering, BIHER conducted on 3.09.2019 & 4.09.2019




COORDINATOR

Ms. R. Hema

Dr. B. Karthik



CONVENOR

Dr. M. Sangeetha