

Bharath Institute of Higher Education and Research
[Declared Under Section 3 of UGC Act, 1956] Chennai – 600 073

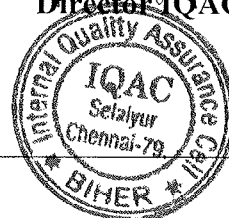
INTERNAL QUALITY ASSURANCE CELL
DOCUMENT SUBMISSION FORM
IQAC

Date of Submission	17/02/2020
Type of Documents	Workshop
Description	Two days workshop on ANALOG VLSI DESIGN
Enclosures	a) Requisition Letter
	b) Permission Letter
	c) Circular
	d) Schedule
	e) Invitation
	f) Brochure
	g) List of Participants
	h) Feedback
	i) Photo
	j) Certificate
No. of Pages	15
Submitted By	Name : Dr.M.Sangeetha
	Designation: Professor & Head
	Department: Electronics and communication Engineering
	Signature:

For Office Use Only

Verified By:	K. Sakthivel	Sign:	Date: 17/2/2020
Uploaded By:	K. Senthil Kumar	Sign:	Date: 17/2/2020
File Name:	SEE - ECE - WORK - 2019 - 2020 - 10		

Director IQAC - BIHER





REQUISITION LETTER

Date 27.01.2020

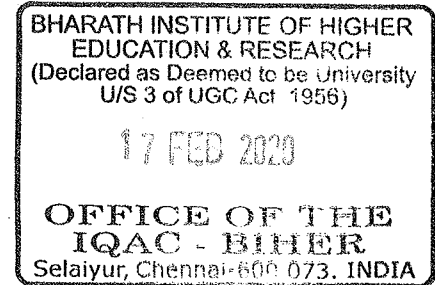
FROM

The HOD
Department of ECE
Bharath Institute of Higher Education and Research
Selaiyur, Chennai-73

TO

The Dean Engineering
Bharath Institute of Higher Education and Research
Selaiyur, Chennai-73

Respected Sir,



Subject: Requisition for Conducting Workshop -reg

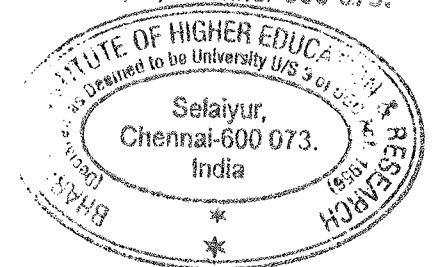
The School of Electrical Engineering has planned to conduct two days' workshop on the topic "**Analog VLSI Design**" in association with "Center for Entrepreneurship Development, Madurai" dated on **12.02.2020 & 13.02.2020**. In this regard, I request you to kindly grant permission for conducting the same

Thanking You

HOD/ECE

Dean Engineering

DEAN (Engineering)
Bharath Institute of Science & Technology
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
(Declared as Deemed to be University U/S 3 of UGC Act. 1956)
Selaiyur, Chennai-600 073.





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INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Permission Letter

Date :29.01.2020

From

The HOD,

Department of ECE,

School of Electrical Engineering,

BIHER.

To

Mr.MahendranKoneti,

Design Engineer,

Microchip Technologies,

Chennai.

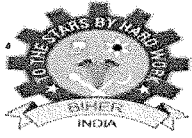
Respected sir,

Sub: -The Department of Electronics and Communication Engineering, School of Electrical Engineering, BIHER, proposed to conduct a workshop on **Analog VLSI Design** dated on **12.02.2020 & 13.02.2020**- Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in “**Analog VLSI Design**” in association with “Centre for Entrepreneurship Development, Madurai” on **12.02.2020** .Kindly accept our request and do the needful .

Convener

Dr.M.Sangeetha
HODECE



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Date: 29.01.2020

From
The HOD,
Department of ECE,
School of Electrical Engineering,
BIHER

To
Dr.M.ARUN,
Associate Professor,
Dept of SENSE,
VIT, Vellore.

Respected sir,

Sub: - The Department of Electronics and Communication Engineering, School of Electrical Engineering, BIHER, proposed to conduct a workshop in “**Analog VLSI Design**” dated on **12.02.2020&13.02.2020** - Permission requested to deliver an invited talk – Reg.

In the continuation of telephonic conversation, it is requested to deliver an invited talk in association with “Center for Entrepreneurship Development, Madurai” dated on **13.02.2020**. Kindly accept our request and do the needful.

Convener

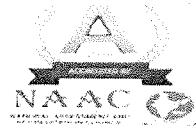
Dr.M.Sangeetha
HOD/ECE



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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

CIRCULAR
SCHOOL OF ELECTRICAL ENGINEERING

Date: 03.02.2020

The Workshop on "Analog VLSI Design" is planned by the School of Electrical Engineering on **12.02.2020 & 13.02.2020**.

This Workshop Programmed is offered by Department Of ECE, Bharath Institute of Higher Education and Research, with the duration of two days (**12.02.2020 & 13.02.2020**). In this Regard, Students are instructed to give their willingness and confirm their participation to their respective class in charge/workshop Coordinator before **07.02.2020**.

Ms.G.Jeyalakshmi, Ms.B.KalaiSelvi

Assistant Professor/ECE, School of electrical Engineering

Workshop Coordinator

Bharath Institute of Higher Education and Research

Email id: jeyalakshmi.ece@bharathuniv.ac.in

Convener:

Dr.M.Sangeetha,

HOD/ECE



SCHOOL OF ELECTRICAL ENGINEERING
WORKSHOP ON ANALOG VLSI DESIGN

SCHEDULE

12/02/2020

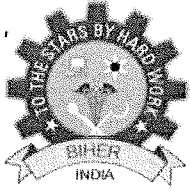
11.00 - 11.15 AM	Inaugural Address
11.20 AM	Welcome Address Dr.M.Sangeetha
11.30 AM	About the Workshop Ms.G,Jeyalakshmi
11.40AM	Introduction of the Chief Guest Dr. Hameed Hussain
11.45 AM	Inaugural address by Chief Guest Mr.MahendranKoneti, Design Engineer, Microchip Technologies, Chennai.
12.00 PM Onwards	Invited Talk Mr.MahendranKoneti, Design Engineer, Microchip Technologies, Chennai.
12.30-1.30 PM	Lunch break
1.30-02.45pm	Session on Recent trends in Analog VLSI Design.
<u>Valedictory program</u>	
02.45-02.50PM	Valedictory Function
02.55-03.00PM	Vote of Thanks (Coordinator)

SCHOOL OF ELECTRICAL ENGINEERING
WORKSHOP ON ANALOG VLSI DESIGN

SCHEDULE

13/02/2020

11.00 AM	Welcome Address Dr.M.Sangeetha
11.15AM	Introduction of the Chief Guest Dr. Hameed Hussain
	Dr.M.ARUN, Associate Professor, Dept of SENSE, VIT,Vellore.
11.30 -12.00 PM	Session on Analog MOSFET Modelling.
12.00 PM-12.45 PM	Session on Low Frequency MOSFET Modelling.
12.45-1.30 PM	Lunch break
1.30-2.00PM	CMOS Comparators
02.30-3.00PM	Basics of RF Design.
<u>Valedictory program</u>	
03.00-03.15PM	Valedictory Function
03.15-03.30PM	Vote of Thanks (Coordinator)



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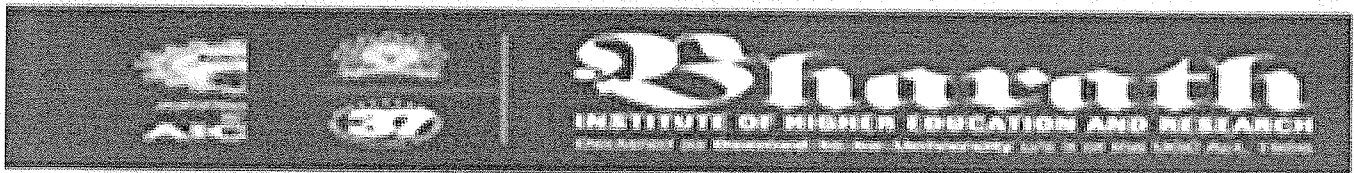
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No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
INVITATION



School of Electrical Engineering
Department of ECE
BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH
in association with

Center for Entrepreneurship Development, Madurai

WORKSHOP ON
ANALOG VLSI DESIGN

Resource Persons: Mr.Mahendran Koneti, Microchip Technologies
Dr.Arun, Associate Prof,VIT,Vellore.

Keynote Address: Dr. J. Hameed Hussain, Dean Engineering, BIHER, Chennai
Special Address: Dr. M. Sangeetha, Professor & Head/ ECE, BIHER
Program Coordinator: Mrs. G. Jeyalakshmi & Mrs. B. Kalaiselvi

Venue : Smart Room, ECE, BIHER
Date : 12.02.2020 & 13.02.2020.
Time : 11.00 am to 3.00 pm



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Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN

BROCHURE

REGISTRATION FORM

1. Name
2. Department
4. Designation
5. Qualification
6. Mobile Number
7. Contact Address:
8. E-Mail ID

REGISTRATION FEE: R5 750

PAYMENT DETAILS:

D.D.No: Date:

Drawn on Bank:

Amount:

Signature Head of Institution

Date:
(Photocopies of the form accepted)

CO-ORDINATOR:

Ms. G. Jayalakshmi & Ms. B. Kalai Selvi
Assistant Professor, ECE

VENUE:

Smart Room
Department of Electronics and
Communication Engineering, BIHER
Chennai.

COURSE CONTENT

- Recent trends in analog VLSI circuits
- Analog MOSFET Modelling
- MOS transistor, Low frequency MOSFET Models
- Bandgap references
- CMOS Amplifier: Performances matrices of amplifier circuits.
- Common source amplifier ARM processor interfacing
- CMOS Comparator: Characteristic of a comparator

RESOURCE PERSONS

1. Mr. Mahendran Koncei

Designer Engineer

Microchip Technologies, Chennai.

2. Dr.M. Arun Anandate Professor

Department of SENSE

WIE, Vellore.

CONVENER

Dr. M. Subramanian

Prof & Head/ECE



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**BHARATH INSTITUTE OF HIGHER
EDUCATION AND RESEARCH**
173, Agharam Road, Selaiyur, Chennai-600 073

**WORKSHOP
ON
ANALOG VLSI DESIGN**
12th and 13th FEBRUARY 2020

**ORGANIZED
BY**
DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING
SCHOOL OF ELECTRONIC SCIENCES



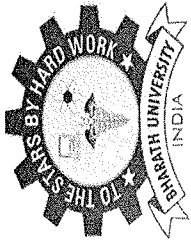
www.bharathbiher.ac.in



Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
PARTICIPANTS LIST
TOTAL NUMBER OF PARTICIPANTS:40

S.NO	STUDENTS NAME	NAME OF THE COLLEGE	CATEGORY	STATE
1	U17EC077-VELAMALA ANIL KUMAR	Bharath University	Student	TamilNadu
2	U17EC501-Hariprasath S	Bharath University	Student	TamilNadu
3	U17EC702-VIJIN S	Bharath University	Student	TamilNadu
4	U17EC078-SURA LAKSHMI NANDITHA REDDY	Bharath University	Student	TamilNadu
5	U17EC079-PADAMATA MANIKANTA SWAMY	Bharath University	Student	TamilNadu
6	U17EC080-MENDU SRI SATYA JAGADISWARA REDDY	Bharath University	Student	TamilNadu
7	U17EC081-PERATI SUSHANTH REDDY	Bharath University	Student	TamilNadu
8	U17EC082-AKSHAY TEEGALA	Bharath University	Student	TamilNadu
9	U17EC083-RAMAKURTHI JAGADISH CHANDRA	Bharath University	Student	TamilNadu
10	U17EC084-SUTHAPALLI KRISHNA KARTHIK	Bharath University	Student	TamilNadu
11	U17EC085-GUDI AKSHITH REDDY	Bharath University	Student	TamilNadu
12	U17EC086-MANNAM BHARATH KUMAR	Bharath University	Student	TamilNadu
13	U17EC087-BADDELA HEMALATHA	Bharath University	Student	TamilNadu
14	U17EC088-K BHEEMESH	Bharath University	Student	TamilNadu
15	U17EC090-LINGAMPALLI YASHWITHA	Bharath University	Student	TamilNadu
16	U17EC091-POTHINI AKHIL CHAKRAVARTHY	Bharath University	Student	TamilNadu
17	U17EC092-SUTHAPALLI J V V RAMA KRISHNA	Bharath University	Student	TamilNadu
18	U17EC093-SEELAM RAVINDER REDDY	Bharath University	Student	TamilNadu
19	ISHWARYA	SRM College of Engineering and Technology	Student	TamilNadu

20	JAYED AHMED	Dhanalakshmi College of Engineering	Student	TamilNadu
21	KUMARI PRIYANKA	Dhanalakshmi College of Engineering	Student	TamilNadu
22	ANIS MD	Dhanalakshmi College of Engineering	Student	TamilNadu
23	MURALITHARAN	Dhanalakshmi College of Engineering	Student	TamilNadu
24	NABIL NAIDU	KPR Institute of Engineering and Technology	Student	TamilNadu
25	PAVITHRA	Bharath University	Student	TamilNadu
26	NAVEEN	SNS College of Technology	Student	TamilNadu
27	NEERAJ	SNS College of Technology	Student	TamilNadu
28	DEEPSHIKA KUMARI	SNS College of Technology	Student	TamilNadu
29	HARESH	SNS College of Technology	Student	TamilNadu
30	SARATH BABU	Bharath University	Student	TamilNadu
31	TAMIZH THENDRAL	Bharath University	Student	TamilNadu
32	SUGENTHAN	Bharath University	Student	TamilNadu
33	UDHAYAN	SNS College of Technology	Student	TamilNadu
34	LOKESH	Bharath University	Student	TamilNadu
35	JAISHANKAR	SRM College of Engineering and Technology	Student	TamilNadu
36	ABHISHEK	Agni College of Technology	Student	TamilNadu
37	HARESH	Agni College of Technology	Student	TamilNadu
38	SRIJAN KUMAR	SRM College of Engineering and Technology	Student	TamilNadu
39	SADHANA	SRM College of Engineering and Technology	Student	TamilNadu
40	SUMIT	Dhanalakshmi College of Engineering	Student	TamilNadu



BSRB

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ACCREDITED WITH 'A' GRADE BY NAAC

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATION OF PARTICIPATION

This is to certify that KUMARI PRIYANKA has attended two days workshop on "Analog VLSI Design" Organized by the Department of Electronics and Communication Engineering, BIHER conducted on 12.02.2020 & 13.02.2020

COORDINATOR

Ms.G.Jeyalakshmi,

Ms.B.KalaiSelvi

CONVENOR

Dr.M.Sangeetha



SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
WORKSHOP ON ANALOG VLSI DESIGN

FEED BACK FORM		Date: 13.02.2019			
Name	Ishwarya				
Phone number	9177766620				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used				✓	
Presentation hand outs					✓

Ishwarya
Student Signature



SCHOOL OF ELECTRICAL ENGINEERING
FEEDBACK FORM
WORKSHOP ON ANALOG VLSI DESIGN

FEED BACK FORM		Date: 12.02.2020			
Name	Sadhana				
Phone number	9224488222				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	


Student Signature

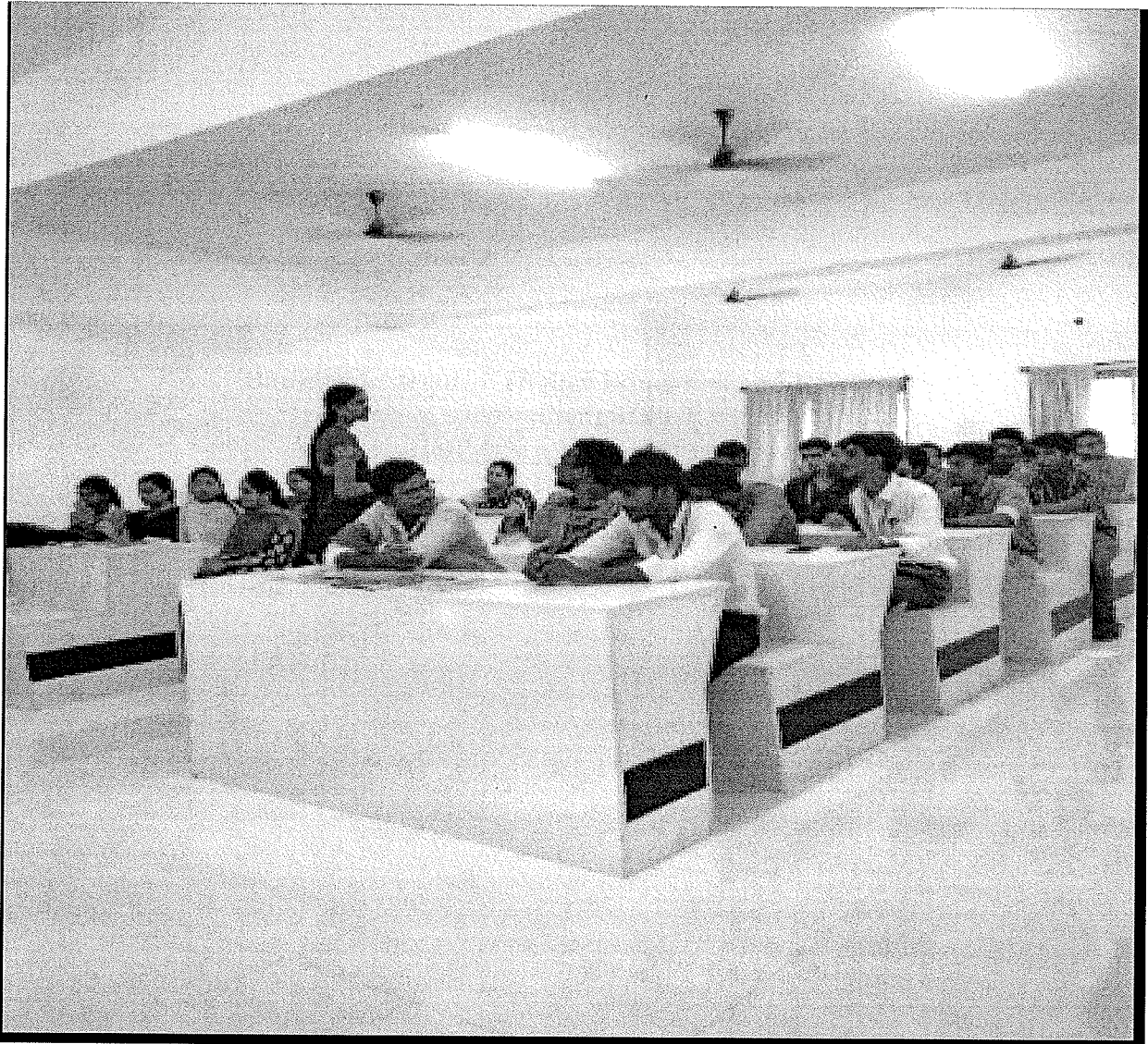


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No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Bharath Institute of Higher Education and Research
Department of ECE
WORKSHOP ON ANALOG VLSI DESIGN
PHOTOS



Students are attending a Workshop on “Analog VLSI Design” on 12.02.2020 & 13.02.2020.