

<b>Course Number and Name</b>												
BEE7L3 & Electrical Drives & Control Laboratory												
<b>Credits and Contact Hours</b>												
2 & 45												
<b>Course Coordinator's Name</b>												
Mr.S.Sherine												
<b>Course Objectives</b>												
To enable the students to design gating circuits, PWM inverters and SMPS												
<b>Prerequisites</b>						<b>Co-requisites</b>						
Nil						Electrical Drives and Control						
required, elective, or selected elective (as per Table 5-1)												
Required												
<b>Course Outcomes (COs)</b>												
CO1: To design various types of gate pulse generating circuits.												
CO2: To interface motors with DSP for speed control applications												
CO3: To simulate various types of converters												
CO4: To design PWM Inverters												
CO5: To design various types of SMPS – BUCK,BOOST & BUCK-BOOST												
<b>Student Outcomes (SOs) from Criterion 3 covered by this Course</b>												
COs/SOs	a	b	c	d	e	f	g	h	i	j	k	l
CO1		H		H		L			M			
CO2		H		H		L			M			
CO3		H		H		L			M			
CO4		H		H		L			M			
CO5		H		H		L			M			