

<b>Course Number and Name</b>	
BEE604 & DIGITAL SIGNAL PROCESSING	
<b>Credits and Contact Hours</b>	
3 & 45	
<b>Course Coordinator's Name</b>	
Dr.S.P.Vijayaragavan	
<b>Text Books and References</b>	
<b>Text Books:</b>	
<ol style="list-style-type: none"> <li>1. J.G. Proakis and D.G. Manolakis, 'Digital Signal Processing Principles, Algorithms and Applications', Pearson Education/ PHI, 4<sup>th</sup> Edition, New Delhi, 2007.</li> <li>2. S.K. Mitra, 'Digital Signal Processing – A Computer Based Approach', Tata McGrawHill, 3<sup>rd</sup> Edition, New Delhi, 2008.</li> </ol>	
<b>References:</b>	
<ol style="list-style-type: none"> <li>1. Alan V. Oppenheim, Ronald W. Schafer and John R. Buck, 'Discrete – Time Signal Processing', Pearson Education, New Delhi, 2003.</li> <li>2. Emmanuel C Ifeachor and Barrie W Jervis, 'Digital Signal Processing – A Practical Approach' Pearson Education, Second edition, 2002.</li> <li>3. Steven W. Smith, "The Scientist and Engineer's Guide to Digital Signal Processing", Second Edition, California Technical Publishing San Diego, California. <a href="http://www.DSPguide.com">www.DSPguide.com</a>)</li> <li>4. B. Venkataramani, M. Bhaskar, 'Digital Signal Processors, Architecture, Programming and Applications', Tata McGraw Hill, New Delhi, 2003</li> </ol>	
<b>Course Description</b>	
<ul style="list-style-type: none"> <li>• To classify signals and systems &amp; their mathematical representation.</li> <li>• To analyse the discrete time systems.</li> <li>• To study various transformation techniques &amp; their computation.</li> <li>• To study about filters and their design for digital implementation.</li> <li>• To study about a programmable digital signal processor &amp; quantization effects</li> </ul>	
<b>Prerequisites</b>	<b>Co-requisites</b>
Mathematics-III	Nil
required, elective, or selected elective (as per Table 5-1)	
Required	
<b>Course Outcomes (COs)</b>	
CO1: Explain Properties and algorithms for implementation of DFT. CO2: Filters Describe and their structures. CO3: Illustrate the design of FIR and IIR filters. CO4: Describe the quantization effects. CO5: Relate the architectures and instruction set of a Digital Signal Processor.	

**Student Outcomes (SOs) from Criterion 3 covered by this Course**

COs/SOs	a	b	c	d	e	f	g	h	i	j	k	l
CO1	H	H	M	H	L	L	M	H	H	H	H	H
CO2	H	H	H	H	H	L	M	H	H		H	H
CO3	H	H	H	H	H			H	H	H	H	H
CO4	M	M	L	H	M			H	H		H	M
CO5	H	H	H	H	H	M	M	H	H			H

**List of Topics Covered**

**UNIT I INTRODUCTION 9**

Classification of systems: Continuous, discrete, linear, causal, stable, dynamic, recursive, time variance; classification of signals: continuous and discrete, energy and power; mathematical representation of signals; spectral density; sampling techniques, quantization, quantization error, Nyquist rate, aliasing effect. Digital signal representation.

**UNIT II DISCRETE TIME SYSTEM ANALYSIS 9**

Z-transform and its properties, inverse z-transforms; difference equation – Solution by z-transform, application to discrete systems - Stability analysis, frequency response – Convolution – Fourier transform of discrete sequence – Discrete Fourier series.

**UNIT III DISCRETE FOURIER TRANSFORM & COMPUTATION 9**

DFT properties, magnitude and phase representation - Computation of DFT using FFT algorithm – DIT & DIF - FFT using radix 2 – Butterfly structure.

**UNIT IV DESIGN OF DIGITAL FILTERS 9**

FIR & IIR filter realization – Parallel & cascade forms. FIR design: Windowing Techniques – Need and choice of windows – Linear phase characteristics. IIR design: Analog filter design - Butterworth and Chebyshev approximations; digital design using impulse invariant and bilinear transformation - Warping, prewarping – Frequency transformation.

**UNIT V DIGITAL SIGNAL PROCESSORS 9**

Introduction – Architecture – Features – Addressing Formats – Functional modes - Introduction to Commercial Processors