

Requisition Letter

Date: 10.07.2018

From

The HOD,  
ECE Department,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai.

To

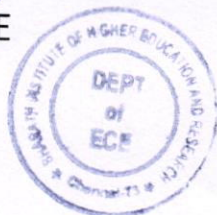
The Dean Engineering,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai.

Respected Sir,

SUB: Requisition for conducting Value Added Course-Regd

School of Electrical Engineering has planned to conduct Value added Course on "**Hand on Training PCB Design and Fabrication**" on 02.08.2018. In this regard we kindly request you to grant permission for the same.

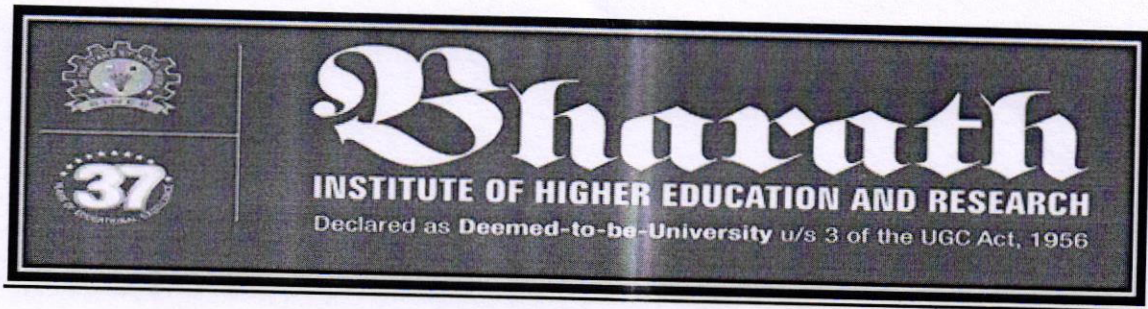
  
HOD/ECE



Thanking you

  
Dean Engineering





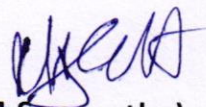
**CIRCULAR**

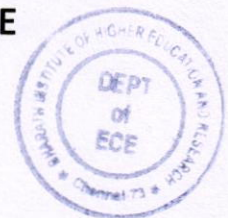
**SCHOOL OF ELECTRICAL ENGINEERING**

**Date: 12.07.2018**

The course on “**Hand on Training PCB Design and Fabrication**” is planned by School of Electrical Engineering which commences on 02-08-2018 (Thursday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

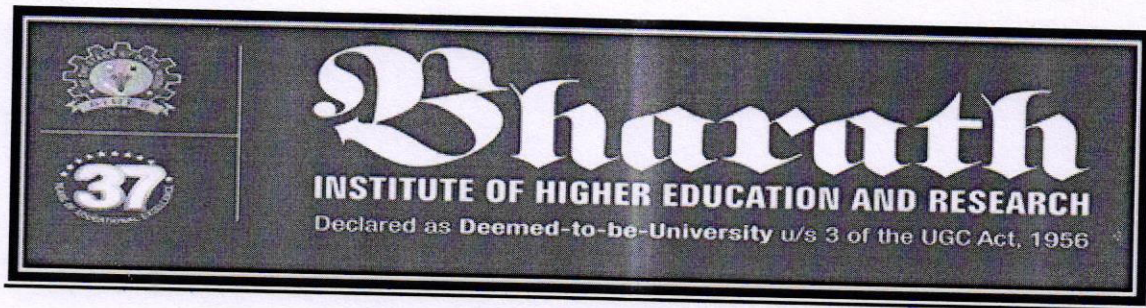
Course Coordinator: B.Hemalatha  
Contact No: 9841403233  
Email id : hemalatha.ece@bharathuniv.ac.in

  
(Dr.M.Sangeetha)  
HOD/ECE



To,  
Copy to ECE Department,  
Copy to EEE Department,  
Department Notice Board





## SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2018 -2019)

### Hand on Training PCB Design and Fabrication

#### Course Objective

This is a basic course for designing of PCB using software. PCB (Printed Circuit Board) designing is an integral part of each electronics products and this program is designed to make students capable to design their own projects PCB up to industrial grade. Learn to draw schematics, convert schematics to layout. Do pre-production planning, printing, CNC operator, quality inspector, production job in a fixed timeline. Designing single/double side PCB. CNC programming. Routing techniques. Planning. Screen printing. Plating. Manual drilling. Inspection. Photo tooling.

#### Resource Persons :

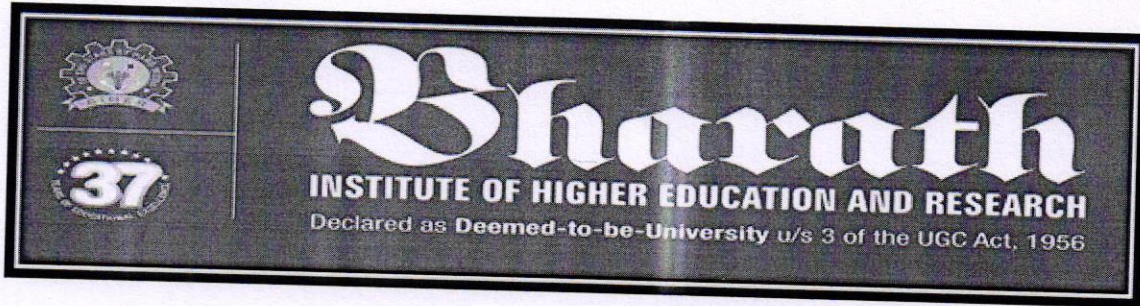
1. Dr.S.Arulselvi
2. Ms. M.Sowmiya Manoj
3. Mr. S.Balaji

  
Convener

Dr.M.Sangeetha  
HOD/ECE







## SCHOOL OF ELECTRICAL ENGINEERING

### Hand on Training PCB Design and Fabrication

#### SCHEDULE

**Contact Hours : 32 hrs**

DATE	SESSION	Contact Hours	TOPICS	Resource person
2-8-2018	FN	9.00 am to 12.30 pm	Brief History of EDA ,Latest Trends in Market , Different EDA tools, Introduction to SPICE and PSpice Environment, Introduction and Working of PROTEUS	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	Through Hole Packages-Axial lead, Radial Lead, Single Inline Package(SIP), Dual InlinePackage(DIP),Transistor Outline(TO) o Pin Grid Array(PGA)	Ms. M.Sowmiya Manoj
3-8-2018	FN	9.00 am to 12.30 pm	Through Hole Packages o Metal Electrode Face(MELF),Leadless Chip Carrier(LCC) ,Small Outline Integrated Circuit(SOIC), Quad Flat Pack(QFP) and Thin QFP (TQFP), Ball Grid Array(BGA),Plastic Leaded Chip Carrier(PLCC)	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	Introduction to PCB Design using OrCAD tool,Introduction to PCB Design using PROTEUS tool	Mr. S.Balaji
4-8-2018	FN	9.00 am to 12.30 pm	Schematic Entry , Net listing, PCB Layout Designing ,Prototype Designing, Design Rule Check(DRC), Design For Manufacturing (DFM), PCB Making, Printing,Etching,Drilling, Assembly of components	Ms. M.Sowmiya Manoj
	AN	1.30 pm to 4 pm	Description of PCB Layers,Electrical Layers-Top Layer, Mid Layer, Bottom Layer ,Mechanical Layers,Board Outlines and Cutouts,Drill Details, Documentation Layers,Components Outlines,Reference	Mr. S.Balaji



			Designation ,Text	
6-8-2018	FN	9.00 am to 12.30 pm	IPC Standard For Schematic Design,IPC Standard For PCB Designing IPC Standard For PCB Materials,IPC Standard For Documentation and PCB Fabrication	Dr.S.Arulselvi
	AN	1.30 pm to 5 pm	Introduction to Auto routing,Setting up Rules,Defining Constraints, Auto router Setup	Ms. M.Sowmiya Manoj
7-8-2018	FN	9.00 am to 12.30 pm	PCB Designing of Basic and Analog Electronic Circuits, PCB Designing of Power Supplies,PCB Designing of Different Sensor modules,PCB Designing of Electronics Projects,PCB Designing of Embedded Projects	Mr. S.Balaji
	AN	1.30 pm to 5 pm	Printing the Design-Etching,Drilling, Interconnecting and Packaging electronic Circuits (IPC) Standards,Gerber Generation,Soldering and De-soldering, Component Mounting,PCB and Hardware Testing	Ms. M.Sowmiya Manoj



## VALUE ADDED COURSE

### SCHOOL OF ELECTRICAL ENGINEERING

#### Hand on Training PCB Design and Fabrication

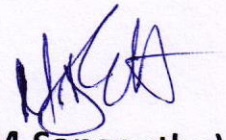
#### List Of Participants

Date: 02.08.2018

Sl.No	REG.NO	NAME OF THE CANDIDATE
1	U13EC006	ARAVIND V
2	U13EC008	ASHIQUE ALAM
3	U13EC021	JAYED AHMED LASKAR
4	U13EC023	KUMARI PRIYANKA
5	U13EC036	SAGNATI VAISHNU VERMA RAJU
6	U13EC038	SHRUTHI T S
7	U13EC039	SHUBHAM KUMAR
8	U13EC042	UTTAM KUMAR
9	U13EC043	VARSHA KUMARI
10	U13EC046	YAMINISHA K
11	U13EC047	ZAHID AHMED
12	U13EC048	BHUVANAPRIYA R
13	U13EC504	VIGNESH M
14	U13EC507	THAMIZH THENDRAL R
15	U13EC702	SUGENTHAN .T
16	U14EC008	ADIREDDY PRAVEEN
17	U14EC010	AMARJEET KUMAR
18	U14EC011	R AMULYA
19	U14EC019	BEDDINTI PRAVEEN KUMAR
20	U14EC108	PONNAGANTI MANOJ DEEP
21	U14EC024	CHANDRALEKA.K
22	U14EC029	DEENADHAYALAN.A
23	U14EC036	EJJAGIRI PRAVEEN
24	U14EC042	GOURU VENKATA SAI PRAKASH
25	U14EC130	P SHOPIC
26	U14EC131	SINGAMREDDY MUKUNDESWAR REDDY
27	U14EC137	SRILADAGUDAM VANGATE SHALINI
28	U14EC139	SRIRAMULA PRANAV
29	U14EC144	SYED NAZIM PASHA KHADRI



30	U14EC103	PAPUGANI PARTHASARADHI.
31	U14EC146	TAMIL SELVI .K
32	U14EC151	THOODI SHEKAR REDDY
33	U13EE011	DEEPAK KUMAR
34	U13EE026	PRAVEEN M
35	U14EE054	YOGESHWARAN .D



(Dr.M.Sangeetha)

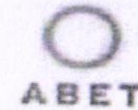
HOD/ECE







**Bharath**  
INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



SCHOOL OF ELECTRICAL ENGINEERING

## CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms SRIRAMULA PRANAV (U14EC139)  
has attended Value added Course On “Hand on Training PCB Design  
and Fabrication” organized by the School of Electrical Engineering,  
BIHER conducted from 02-08-2018 to 07-08-2018.

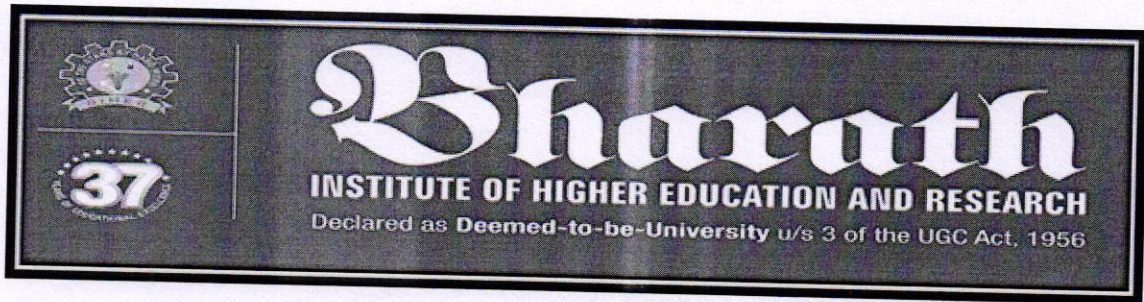
B. Hemalatha

B.HEMALATHA  
COURSE COORDINATOR

Dr. M. Sangeetha

Dr.M.SANGEETHA  
CONVENOR





**SCHOOL OF ELECTRICAL ENGINEERING**

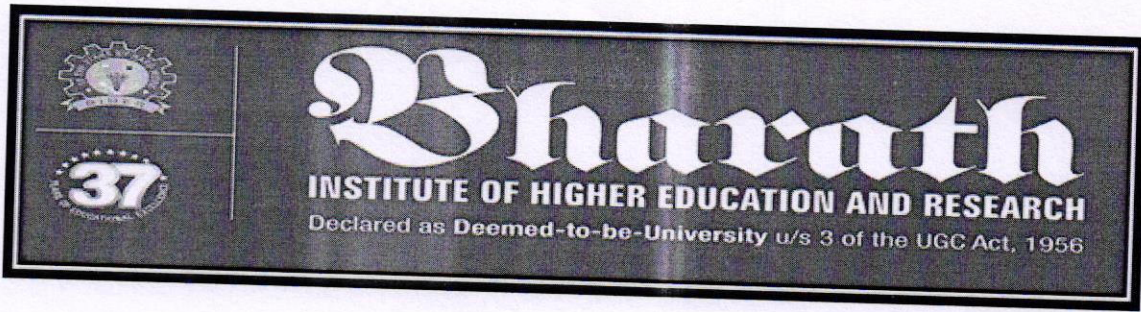
**VALUE ADDED COURSE**

**Hand on Training PCB Design and Fabrication**

FEED BACK FORM		Date: 08.08.2018			
Name	VIGNESH.M				
Register number	U13EC504				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker					✓
Audio, Visual Aids Technology used				✓	
Presentation hand outs			✓		

  
Student Signature





**SCHOOL OF ELECTRICAL ENGINEERING**

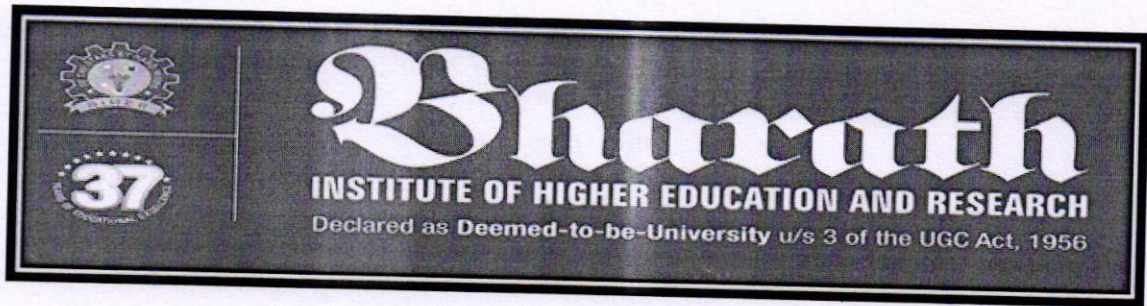
**VALUE ADDED COURSE**

**Hand on Training PCB Design and Fabrication**

FEED BACK FORM		Date: <u>08.08.2018</u>			
Name	Praveen.M.				
Register number	U13EE026				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					
The Speaker					
Audio, Visual Aids Technology used					
Presentation hand outs					

*Praveen*  
Student Signature





## SCHOOL OF ELECTRICAL ENGINEERING

Course on "Hand on Training PCB Design and Fabrication" dated on 02.08.2018 conducted by school of Electrical Engineering

