

Requisition Letter

Date: 04.06.2018

From

The HOD,  
ECE Department,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai.

To

The Dean Engineering,  
Bharath Institute of Higher Education and Research,  
Selaiyur, Chennai.

Respected Sir,


SUB : Requisition for conducting Value Added Course-Regd

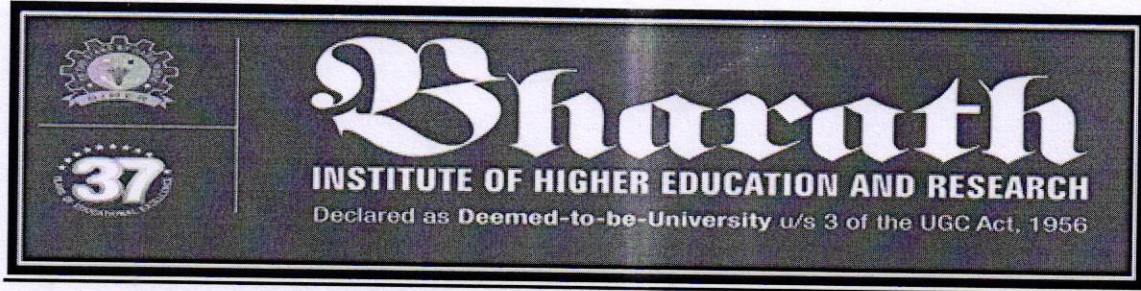
School of Electrical Engineering has planned to conduct Value added Course on "Hands on Training using Cadence for Digital Ic Design" on 04.07.2018. In this regard we kindly request you to grant permission for the same.

Thanking you

  
HOD/ECE



  
Dean Engineering




**CIRCULAR**

**SCHOOL OF ELECTRICAL ENGINEERING**

**Date: 15.06.2018**

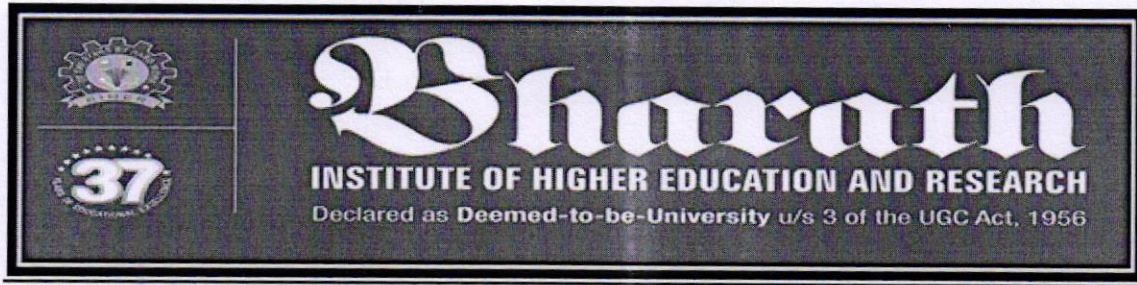
The course on Hands on Training using Cadence for Digital Ic Design is planned by School of Electrical Engineering which commences on 04.07.2018 (Wednesday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj  
Contact No:7358747803  
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

  
(Dr.M.Sangeetha)  
HOD/ECE



To,  
Copy to ECE Department,  
Copy to EEE Department,  
Department Notice Board



## SCHOOL OF ELECTRICAL ENGINEERING

**Value Added Courses (2018 -2019)**


### Hands on Training using Cadence for Digital Ic Design

#### Course Objective

The course on Digital and Mixed Signal IC design has an emphasis to provide a comprehensive design flow on Digital, Analog and Mixed signal IC design. Mixed-signal IC designs are the fastest growing market segments in the electronics and semiconductor industry with applications in diverse field. This course aims to provide practical exercises using the Cadence Encounter Digital Implementation and Analog Virtuoso design environment. The hands-on sessions shall fine-tune the skills of students, interested/working in the field of Analog/Digital/Mixed signal IC design. This course bridges the gap between college curriculum and Industry demand by building the systems from core concepts to advanced level.

#### **Resource Persons :**

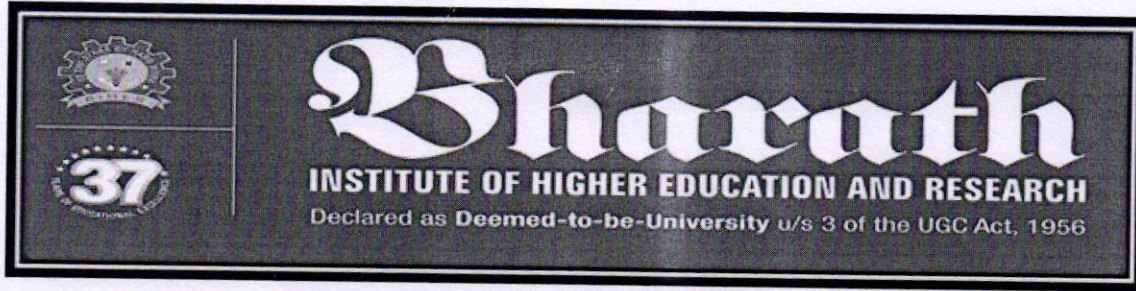
- 1.Ms.B.Hemalatha
- 2.Ms.S.Balaji
- 3.Ms.S.Saravana

  
Convener

**Dr.M.Sangeetha**

**HOD/ECE**





## SCHOOL OF ELECTRICAL ENGINEERING

### Hands on Training using Cadence for Digital Ic Design

#### SCHEDULE

**Contact Hours : 31 hrs**

DATE	SESSION	Contact Hours	TOPICS	Resource person
04.07.2018	FN	9.00 am to 12.30 pm	Digital design flow	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Analog design flow	Ms.S.Balaji
05.07.2018	FN	9.00 am to 12.30 pm	Mixed signal design concepts	Ms.S.Saravana
	AN	1.30 pm to 4 pm	Sample and Hold circuit	Ms.B.Hemalatha
06.07.2018	FN	9.00 am to 12.30 pm	Common source and differential amplifier	Ms.S.Balaji
	AN	1.30 pm to 4 pm	Operational amplifier	Ms.S.Saravana
09.07.2018	FN	9.00 am to 12.30 pm	Design of ADC and DAC	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Small signal and large signal analysis	Ms.S.Balaji
10.07.2018	FN	9.00 am to 12.30 pm	Monte Carlo Simulation	Ms.S.Saravana
	AN	1.30 pm to 5 pm	VLSI Layout design (using Cadence Virtuoso Layout Editor)	Ms.B.Hemalatha

**VALUE ADDED COURSE**

**SCHOOL OF ELECTRICAL ENGINEERING**

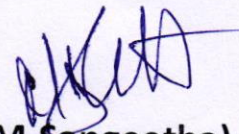
**Hands on Training using Cadence for Digital Ic Design**

**List Of Participants**

**Date:04.07.2018**

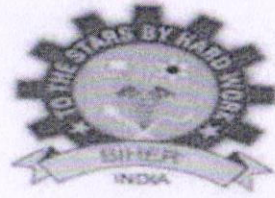
<b>Sl.No</b>	<b>REG.NO</b>	<b>NAME OF THE CANDIDATE</b>
1	U13EC001	ABINAYA P
2	U13EC002	AKHIRUL ISLAM
3	U13EC003	ANAND RAHUL
4	U13EC004	ANGEL M
5	U13EC005	ANJALI KUMARI
6	U13EC006	ARAVIND V
7	U13EC008	ASHIQUE ALAM
8	U13EC009	ASHVATHAMAN K
9	U13EC010	BABLOO KUMAR
10	U13EC015	DEEPAN R
11	U13EC016	DEEPSHIKHA KUMARI
12	U13EC017	DEVENDRA PRIYANKA SUNDER
13	U13EC018	ELVIS RYNTONG
14	U13EC019	G RAHUL REDDY
15	U13EC020	ISWARYA S
16	U13EC021	JAYED AHMED LASKAR
17	U13EC024	MD ANIS

18	U13EC025	MURALITHARAN N
19	U13EC026	NABIL NAIDU S
20	U13EC040	SUKANTO SHARAN
21	U13EC042	UTTAM KUMAR
22	U13EC043	VARSHA KUMARI
23	U13EC048	BHUVANAPRIYA R
24	U13EC051	LAICHIMA BRAHMA
25	U13EC052	ARUN K
26	U13EC054	PURUSHOTTAM KUMAR
27	U13EC501	BALACHANDER S.P
28	U13EC503	M AKSHAY REDDY
29	U13EC507	THAMIZH THENDRAL R
30	U13EC508	MANCHALA RAHUL
31	U13EC509	NAVEEN N
32	U13EC511	VIGNESH M
33	U13EC517	SUBIN JERALD P
34	U13EE051	SYED KABUL ANSARI F
35	U13EE052	TALEEV AALAM
36	U13EE507	MOHAMED ARIFF M
37	U13EE021	NALINEE SINGH

  
(Dr.M.Sangeetha)

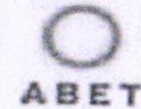
HOD/ECE





# Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)



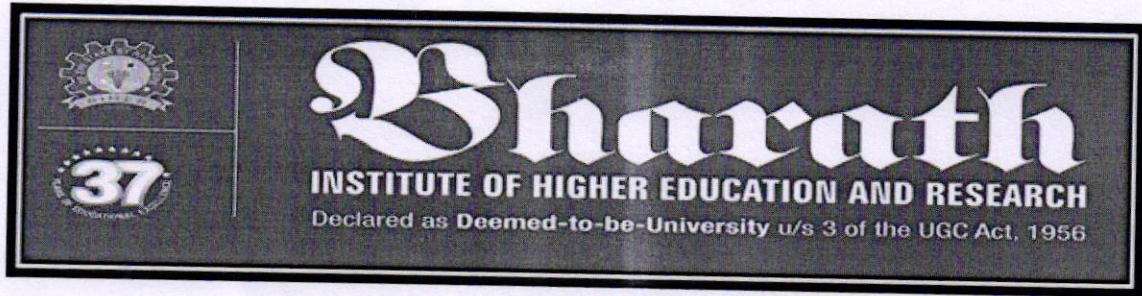
SCHOOL OF ELECTRICAL ENGINEERING

## CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms ARUN . K (U13EC052)  
has attended Value added Course On “*Hands On Training using  
Cadence for Digital Ic Design*” organized by the School of Electrical  
Engineering, BIHER conducted from 04-07-2018 to 10-07-2018.

M.SOWMIYA MANOJ  
COURSE COORDINATOR

Dr.M.SANGEETHA  
CONVENOR



**SCHOOL OF ELECTRICAL ENGINEERING**

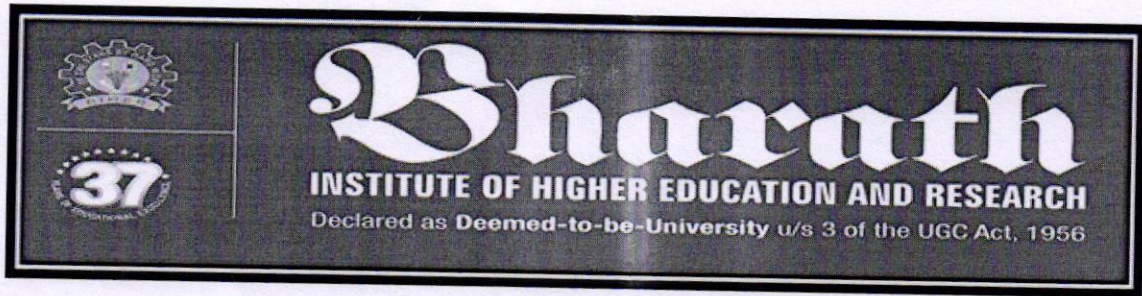
**VALUE ADDED COURSE**

**Hands on Training using Cadence for Digital Ic Design**

FEED BACK FORM		Date:10.07.2018			
Name	Nalinee Singh				
Register number	U13EE021				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs			✓		

  
**Student Signature**





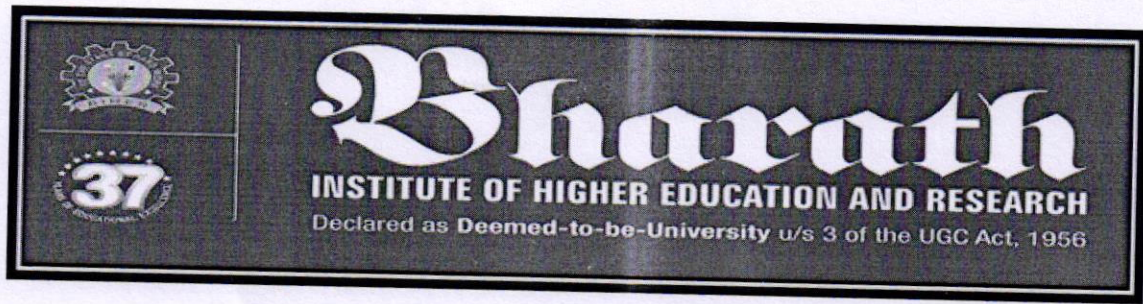
**SCHOOL OF ELECTRICAL ENGINEERING**

**VALUE ADDED COURSE**

**Hands on Training using Cadence for Digital Ic Design**

FEED BACK FORM		Date:10.07.2018			
Name	Akshay Reddy . M .				
Register number	V13EC503				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker					✓
Audio, Visual Aids Technology used				✓	
Presentation hand outs					✓

*akshayreddy*  
Student Signature



## SCHOOL OF ELECTRICAL ENGINEERING

Course on Hands on Training using Cadence for Digital Ic Design dated on 04.07.2018  
conducted by school of Electrical Engineering

