

## SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2019 -2020)

### PCB Designing and Simulation

The course provides an in-depth understanding of top-down device fabrication. Focus is the unit processes typically used in micro & nanofabrication of devices. Both concepts and practical aspects are covered. Topics include crystal growth, doping, chemical vapor deposition, physical vapor deposition, photolithography, wet etching, dry etching, and packaging. The course is accessible to students from diverse backgrounds, such as materials, physics, chemistry, mechanical engineering, and electrical engineering. The course will be a derivative of Advanced Micro & Nano Fabrication Technology & Processes.

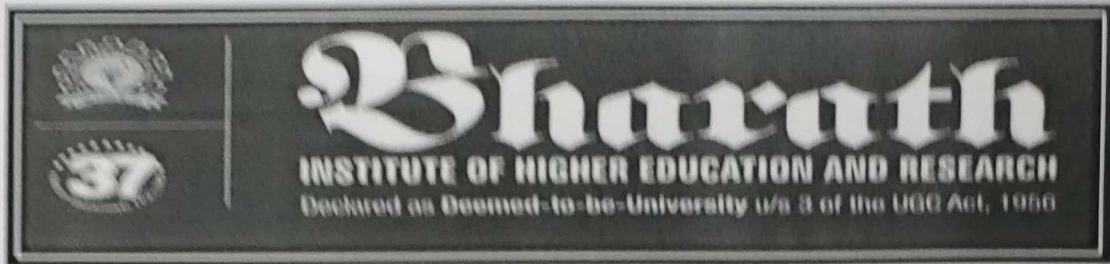
#### Resource Persons:

1. Dr.B.Karthick
2. Mrs R.Hema
- 3.Mrs.S.Philomina

Convener

Dr.M.Sangeetha

HOD/ECE



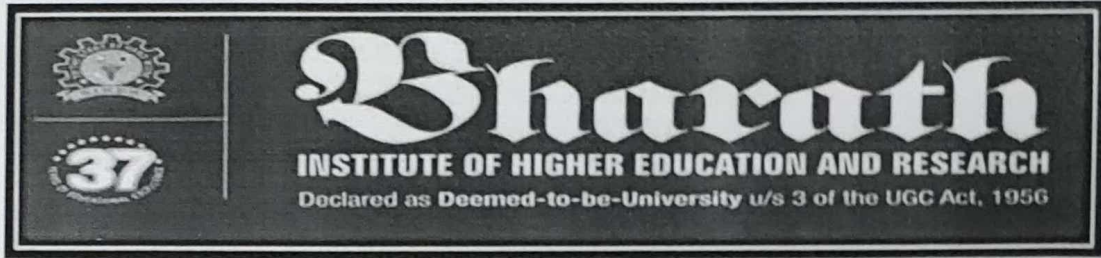
**SCHOOL OF ELECTRICAL ENGINEERING**

**PCB Designing and Simulation**

**SCHEDULE**

**Contact Hours : 31 hrs**

DATE	SESSION	Contact Hours	TOPICS	Resource person
05.08.2019	FN	9.30 am to 1.00 pm	Identifying Electronic Components Symbols & Footprints	Dr.B.Karthick
	AN	1.30 pm to 5 pm	Constructing your component libraries & use them effectively	Mrs R.Hema
06.08.2019	FN	9.30 am to 1.00 pm	Schematic creation & interpretation	Mrs.S.Philomina
	AN	1.30 pm to 5 pm	Effective use of design rules & interfacing between schematic & PCB	Dr.B.Karthick
07.08.2019	FN	9.30 am to 1.00 pm	Component placement & routing techniques for various technologies	Mrs R.Hema
	AN	1.30 pm to 5 pm	Introduction to PCB manufacturing machines Understanding the manufacturing process of PCB	Mrs.S.Philomina
08.08.2019	FN	9.30 am to 1.00 pm	Introduction to the Machines & Developing the negative film	Dr.B.Karthick
	AN	1.30 pm to 5 pm	Copper clad preparation[photo Resist Dip Coating] & Ultraviolet exposure and developing	Mrs R.Hema
09.08.2019	FN	9.30 am to 1.00 pm	Drilling and Etching Process, Printing the PCB layout design, Diagram, Board, Routing.	Mrs.S.Philomina
	AN	1.30 pm to 5 pm	Lab demo and video presentation on PCB designing and simulation	Dr.B.Karthick



**CIRCULAR**

**SCHOOL OF ELECTRICAL ENGINEERING**

**Date: 29.07.2019**

The course on Acquaintance of PCB Designing and Simulation is planned by School of Electrical Engineering which commences on 05.08.2019 (Monday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: Ms.Kalaiselvi B

Contact No:9444649010

Email id : kalaiselvi.ece@bharathuniv.ac.in

**(Dr.M.Sangeetha)  
HOD/ECE**

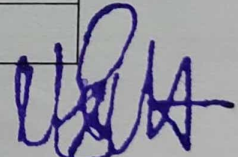
To,  
Copy to ECE Department,  
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Department Notice Board

**VALUE ADDED COURSE**  
**SCHOOL OF ELECTRICAL ENGINEERING**  
**PCB Designing and Simulation**  
**List of Participants**

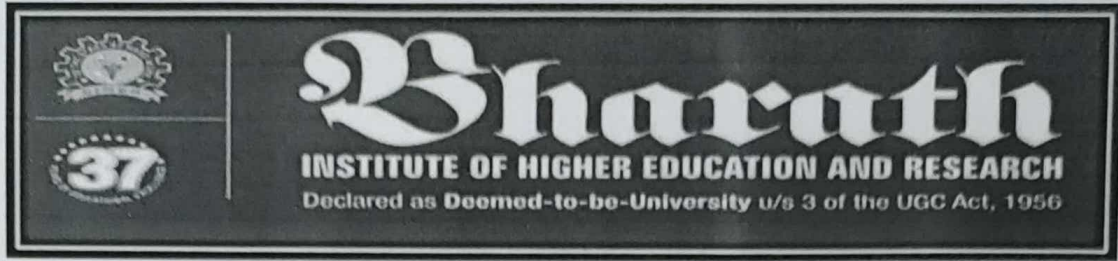
Date: 05.08.2019

S.No	REG.N0	NAME OF THE CANDIDATE
1	U12EC001	ABHISHEK KUMAR
2	U12EC005	ARSHIYA MIZBA
3	U12EC006	AVANI RANJAN
4	U12EC007	BALAJI P
5	U12EC008	BLESSNI VASANTHA N S
6	U12EC010	CHANDESHWAR KUMAR
7	U12EC011	CHINNA ANKANNAGARI ARUNA
8	U12EC014	DINESH KUMAR P
9	U12EC015	GHATTAMANENI LEKHA
10	U12EC017	JANAKI RAMAN S
11	U12EC018	JEBIN SAM B
12	U12EC019	JOTHI KRISHNAN R
13	U12EC020	KAVITHA S
14	U12EC021	KUMAR GAURAV
15	U12EC025	M NAVEENKUMAR GOUD
16	U12EC028	MANIRATNAM KUMAR
17	U12EC029	MANJIT RAY CHOWDHURY
18	U12EC030	MOHAMED KASIM N
19	U12EC038	RAJ
20	U12EC039	RANJITHKUMAR M
21	U12EC044	SANDIP SINGH
22	U12EC045	SAROJ KUMAR
23	U12EC052	SRIVATSAN V
24	U12EC053	SRIVIDHYA S

25	U12EC062	SWETA SADA
26	U12EC063	VAISHALI SINGH
27	U12EC066	VIJAY KUMAR SINGH
28	U12EC067	VIJAY KUMAR SWARNKAR
29	U12EC069	VINAY KUMAR PANDEY
30	U12EC070	WANIL KUMAR
31	U12EC071	JAYA KUMARI
32	U12EC072	SANJEEV KUMAR
33	U12EC073	SUNITA KUMARI
34	U12EC074	MANIKANDAN M
35	U12EC075	MD SOHAIL AKHTAR
36	U12EC076	ARAVINDHAN R
37	U12EC077	PAVITHRA M
38	U12EC502	CLINTON MICHAEL.M
39	U12EC505	ARAVINDH RA
40	U12EC506	VIMALESH T
41	U12EC509	VISHAL B
42	U12EC511	VIJAY ANJAN KUMAR RAGHAVA
43	U12EC512	SATHISHKUMAR A S
44	U12EC521	SYEDUMER A
45	U12EC504	GOVARDAN J
46	U12EC520	ARUN V
47	U12EC522	KIRAN RAAJ K
48	U12EC524	GOVIND RAM P

  
(Dr.M.Sangeetha)

**HOD/ECE**



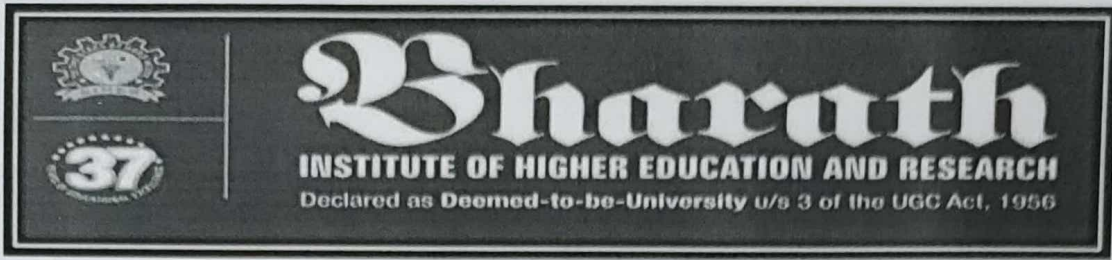
**SCHOOL OF ELECTRICAL ENGINEERING**

**VALUE ADDED COURSE**

**PCB Designing and Simulation**

FEED BACK FORM		Date: 9/8/2019			
Name	Vishal . B				
Register number	U12EC509				
Phone number	9884588267				
Email address	vishal-20@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
The Speaker				✓	
Audio, Visual Aids Technology used					✓
Presentation hand outs				✓	

  
**Student Signature**

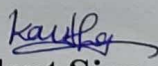


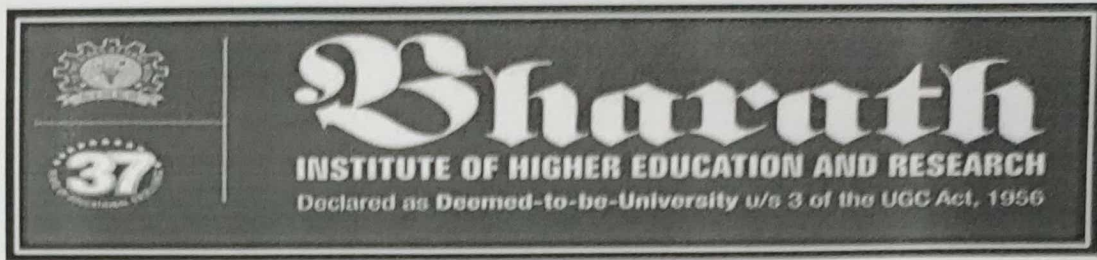
**SCHOOL OF ELECTRICAL ENGINEERING**

**VALUE ADDED COURSE**

**PCB Designing and Simulation**

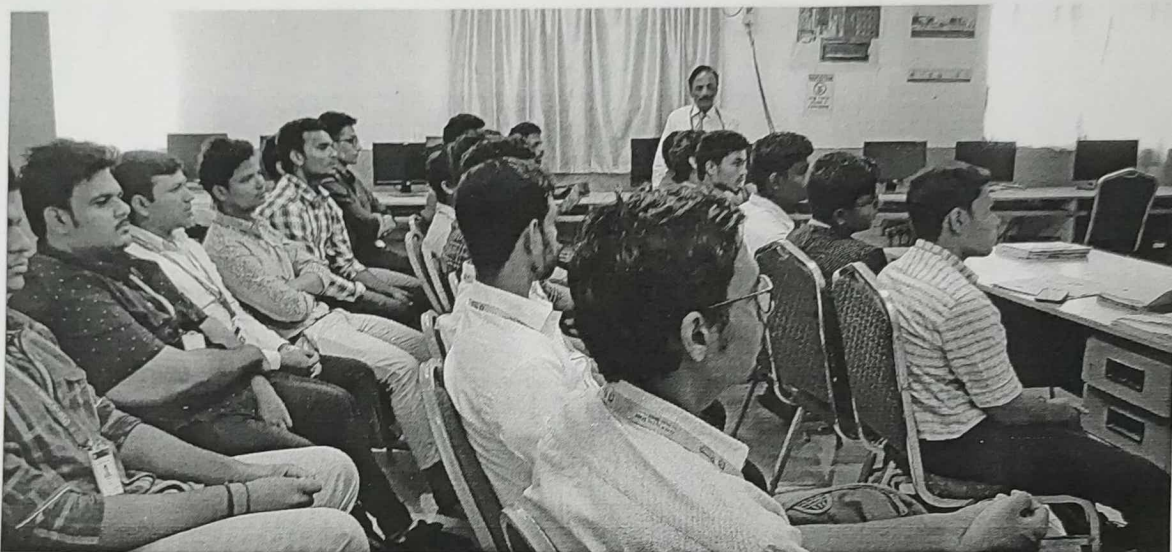
<b>FEED BACK FORM</b>		Date: 9.8.2019			
Name	Kavitha . S				
Register number	U12EC020				
Phone number	9547231832				
Email address	kavithas@gmail.com				
	<b>Poor</b>	<b>Fair</b>	<b>Good</b>	<b>Very Good</b>	<b>Excellent</b>
Overall Program				✓	
The Speaker				✓	✓
Audio, Visual Aids Technology used				✓	
Presentation hand outs				✓	

  
**Student Signature**



## SCHOOL OF ELECTRICAL ENGINEERING

Course on PCB Designing and Simulation dated on 05.08.2019 conducted by School of Electrical Engineering







# BIHER


INSTITUTE OF HIGHER EDUCATION AND RESEARCH  
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)




SCHOOL OF ELECTRICAL ENGINEERING

## CERTIFICATE OF PARTICIPATION

This is to certify that Mr **CHINTALA GANESH [U16EC053]** has attended Value added Course On “PCB Designing and Simulation” organized by the School of Electrical Engineering, BIHER conducted from **05-08-2019 to 09-08-2019.**

  
MS.KALAISEL VI B  
COURSE COORDINATOR

  
Dr.M.SANGEETHA  
CONVENOR